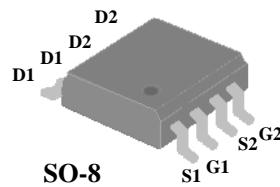




## ▼ Low On-resistance

## ▼ Simple Drive Requirement

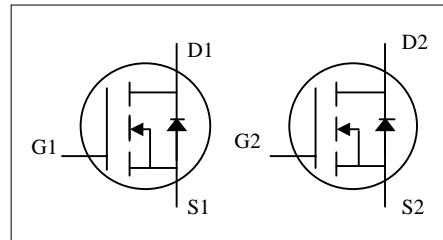
## ▼ Fast Switching Characteristic



$BV_{DSS}$	30V
$R_{DS(ON)}$	26mΩ
$I_D$	6.8A

**Description**

Advanced Power MOSFETs from APEC provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.



The SO-8 package is widely preferred for commercial-industrial surface mount applications and suited for low voltage applications such as DC/DC converters.

**Absolute Maximum Ratings**

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	30	V
$V_{GS}$	Gate-Source Voltage	+ 20	V
$I_D @ T_A = 25^\circ C$	Continuous Drain Current <sup>3</sup>	6.8	A
$I_D @ T_A = 70^\circ C$	Continuous Drain Current <sup>3</sup>	5.5	A
$I_{DM}$	Pulsed Drain Current <sup>1</sup>	40	A
$P_D @ T_A = 25^\circ C$	Total Power Dissipation	2	W
	Linear Derating Factor	0.016	W/°C
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C

**Thermal Data**

Symbol	Parameter	Value	Unit
$R_{thj-a}$	Maximum Thermal Resistance, Junction-ambient <sup>3</sup>	62.5	°C/W



## Electrical Characteristics@ $T_j=25^\circ\text{C}$ (unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\mu\text{A}$	30	-	-	V
$\text{R}_{\text{DS(ON)}}$	Static Drain-Source On-Resistance <sup>2</sup>	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=6\text{A}$	-	-	26	$\text{m}\Omega$
		$\text{V}_{\text{GS}}=4.5\text{V}, \text{I}_D=4\text{A}$	-	-	40	$\text{m}\Omega$
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=250\mu\text{A}$	1	-	3	V
$\text{g}_{\text{fs}}$	Forward Transconductance	$\text{V}_{\text{DS}}=10\text{V}, \text{I}_D=6\text{A}$	-	6	-	S
$\text{I}_{\text{DSS}}$	Drain-Source Leakage Current	$\text{V}_{\text{DS}}=30\text{V}, \text{V}_{\text{GS}}=0\text{V}$	-	-	1	$\text{uA}$
	Drain-Source Leakage Current ( $T_j=70^\circ\text{C}$ )	$\text{V}_{\text{DS}}=24\text{V}, \text{V}_{\text{GS}}=0\text{V}$	-	-	25	$\text{uA}$
$\text{I}_{\text{GSS}}$	Gate-Source Leakage	$\text{V}_{\text{GS}}= \pm 20\text{V}$	-	-	$\pm 100$	nA
$\text{Q}_{\text{g}}$	Total Gate Charge <sup>2</sup>	$\text{I}_D=6\text{A}$	-	7.7	12	nC
$\text{Q}_{\text{gs}}$	Gate-Source Charge	$\text{V}_{\text{DS}}=15\text{V}$	-	1.4	-	nC
$\text{Q}_{\text{gd}}$	Gate-Drain ("Miller") Charge	$\text{V}_{\text{GS}}=4.5\text{V}$	-	4.1	-	nC
$t_{\text{d(on)}}$	Turn-on Delay Time <sup>2</sup>	$\text{V}_{\text{DS}}=20\text{V}$	-	5	-	ns
$t_r$	Rise Time	$\text{I}_D=1\text{A}$	-	8	-	ns
$t_{\text{d(off)}}$	Turn-off Delay Time	$\text{R}_G=3.3\Omega, \text{V}_{\text{GS}}=10\text{V}$	-	18.5	-	ns
$t_f$	Fall Time	$\text{R}_D=20\Omega$	-	9	-	ns
$\text{C}_{\text{iss}}$	Input Capacitance	$\text{V}_{\text{GS}}=0\text{V}$	-	485	770	pF
$\text{C}_{\text{oss}}$	Output Capacitance	$\text{V}_{\text{DS}}=25\text{V}$	-	80	-	pF
$\text{C}_{\text{rss}}$	Reverse Transfer Capacitance	f=1.0MHz	-	75	-	pF
$\text{R}_{\text{g}}$	Gate Resistance	f=1.0MHz	-	1.4	2.1	$\Omega$

## Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
$\text{V}_{\text{SD}}$	Forward On Voltage <sup>2</sup>	$\text{I}_S=2.1\text{A}, \text{V}_{\text{GS}}=0\text{V}$	-	-	1.2	V
$t_{\text{rr}}$	Reverse Recovery Time <sup>2</sup>	$\text{I}_S=6\text{A}, \text{V}_{\text{GS}}=0\text{V},$ $d\text{I}/dt=100\text{A}/\mu\text{s}$	-	19	-	ns
$\text{Q}_{\text{rr}}$	Reverse Recovery Charge		-	11	-	nC

## Notes:

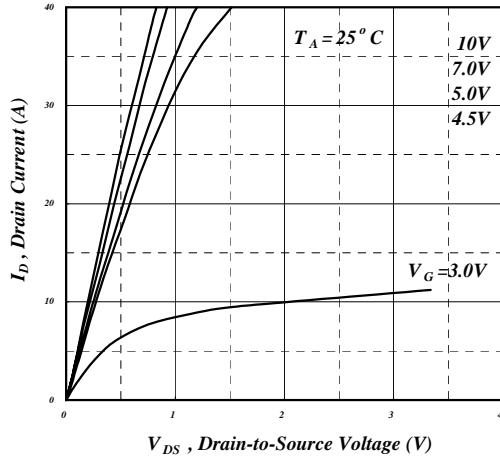
- 1.Pulse width limited by Max. junction temperature.
- 2.Pulse test
- 3.Surface mounted on 1 in<sup>2</sup> copper pad of FR4 board, t  $\leq$  10sec ; 135 °C/W when mounted on Min. copper pad.

THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.

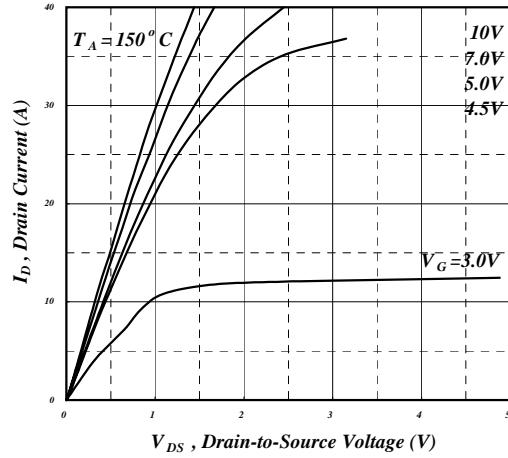
USE OF THIS PRODUCT AS A CRITICAL COMPONENT IN LIFE SUPPORT OR OTHER SIMILAR SYSTEMS IS NOT AUTHORIZED.

APEC DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

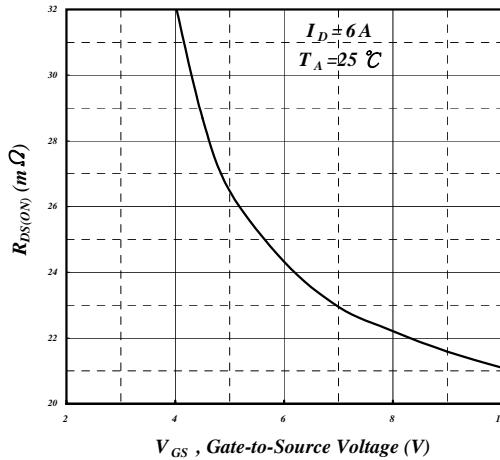
APEC RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN.



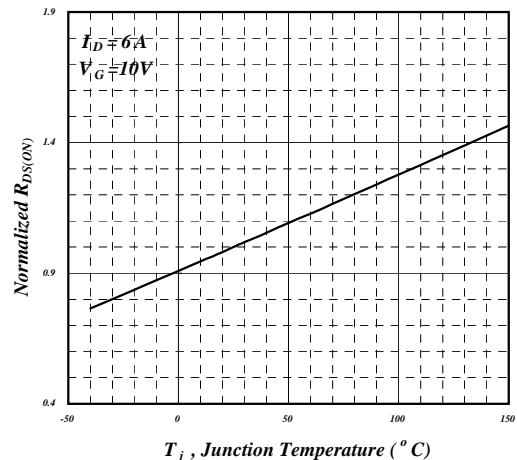
**Fig 1. Typical Output Characteristics**



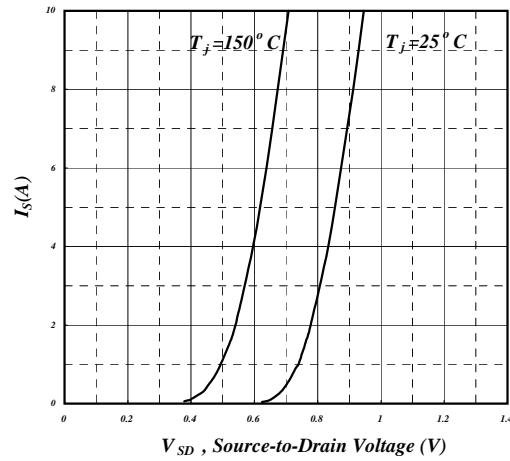
**Fig 2. Typical Output Characteristics**



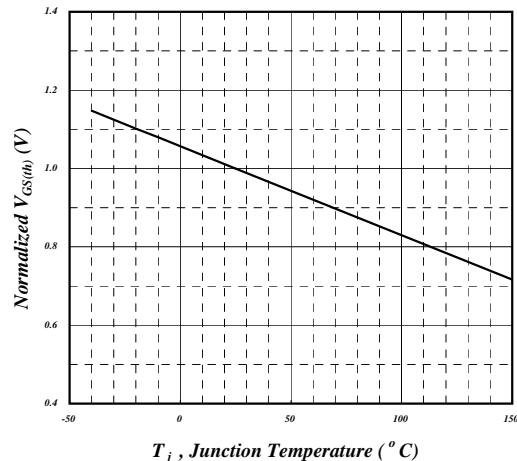
**Fig 3. On-Resistance v.s. Gate Voltage**



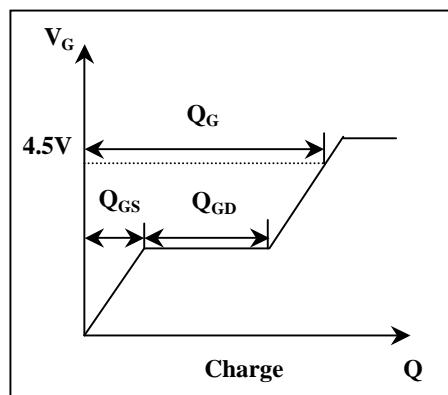
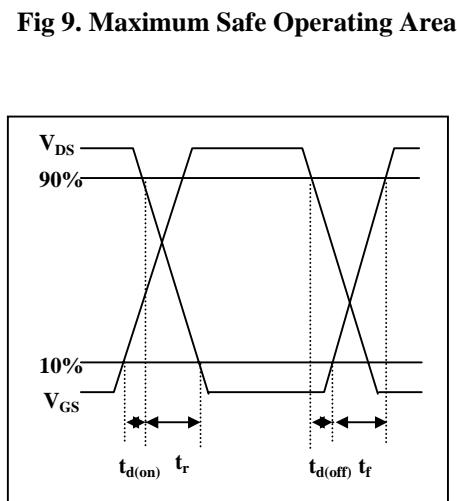
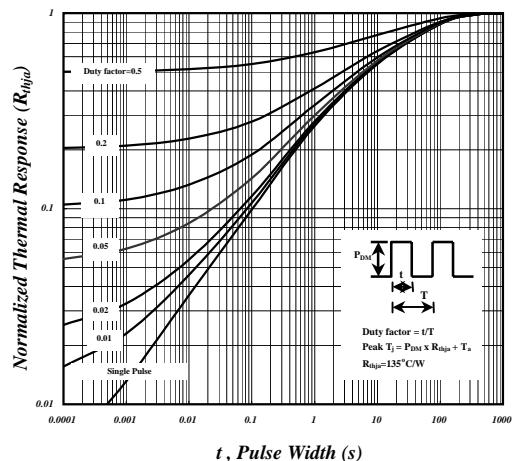
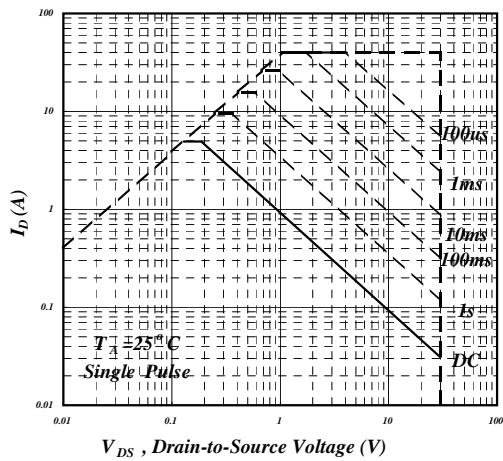
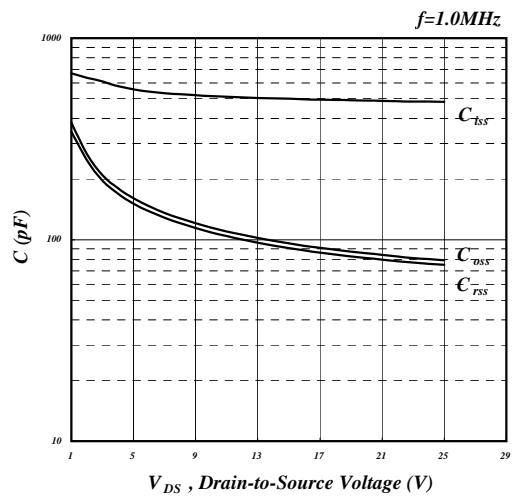
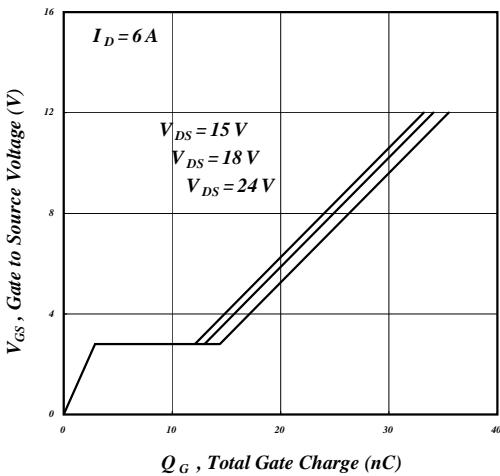
**Fig 4. Normalized On-Resistance v.s. Junction Temperature**



**Fig 5. Forward Characteristic of Reverse Diode**



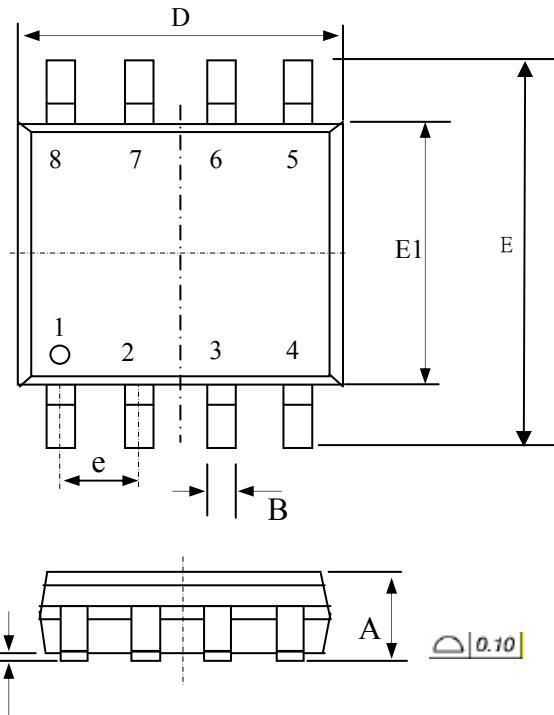
**Fig 6. Gate Threshold Voltage v.s. Junction Temperature**



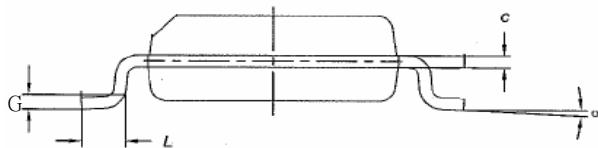


ADVANCED POWER ELECTRONICS CORP.

## Package Outline : SO-8



SYMBOLS	Millimeters		
	MIN	NOM	MAX
A	1.35	1.55	1.75
A1	0.10	0.18	0.25
B	0.33	0.41	0.51
c	0.19	0.22	0.25
D	4.80	4.90	5.00
E	5.80	6.15	6.50
E1	3.80	3.90	4.00
e	1.27 TYP		
G	0.254 TYP		
L	0.38	—	0.90
$\alpha$	0.00	4.00	8.00



1. All Dimension Are In Millimeters.

2. Dimension Does Not Include Mold Protrusions.

## Part Marking Information & Packing : SO-8

