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MAX38640–MAX38643

Tiny 1.8V to 5.5V Input, 330nA I_Q, 700mA nanoPower Buck Converter

General Description

The MAX38640–MAX38643 are a nanoPower family of ultra-low 330nA quiescent current buck (step-down) DC-DC converters that operate from 1.8V to 5.5V input voltage and support load currents of up to 175mA, 350mA, 700mA with peak efficiencies of 96%. While in shutdown, there is only 5nA of shutdown current. The devices offer ultra-low quiescent current, small total solution size, and high efficiency throughout the load range. The MAX38640–MAX38643 are ideal for battery applications where long battery life is a must.

The MAX38640–MAX38643 family utilizes a unique control scheme that allows ultra-low quiescent current and high efficiency over a wide output current range. The MAX38642 excludes active discharge resistor in shutdown, which allows the output to be regulated or held high by another source or by the charged output capacitor.

The MAX38640–MAX38643 devices are offered in a space-saving 1.42mm x 0.89mm, 6-pin WLP (2x3 bump, 0.4mm pitch), as well as a 2mm x 2mm, 6-pin μ DFN package. All parts are specified over the -40°C to +85°C extended temperature range.

Applications

- Portable, Space-Constrained Consumer Products
- Wearable Devices, Ultra-Low-Power IoT, NB IoT, and Bluetooth® LE
- Single Li-ion (Li+) and Coin Cell Battery Products
- Wired or Wireless Industrial Products

Benefits and Features

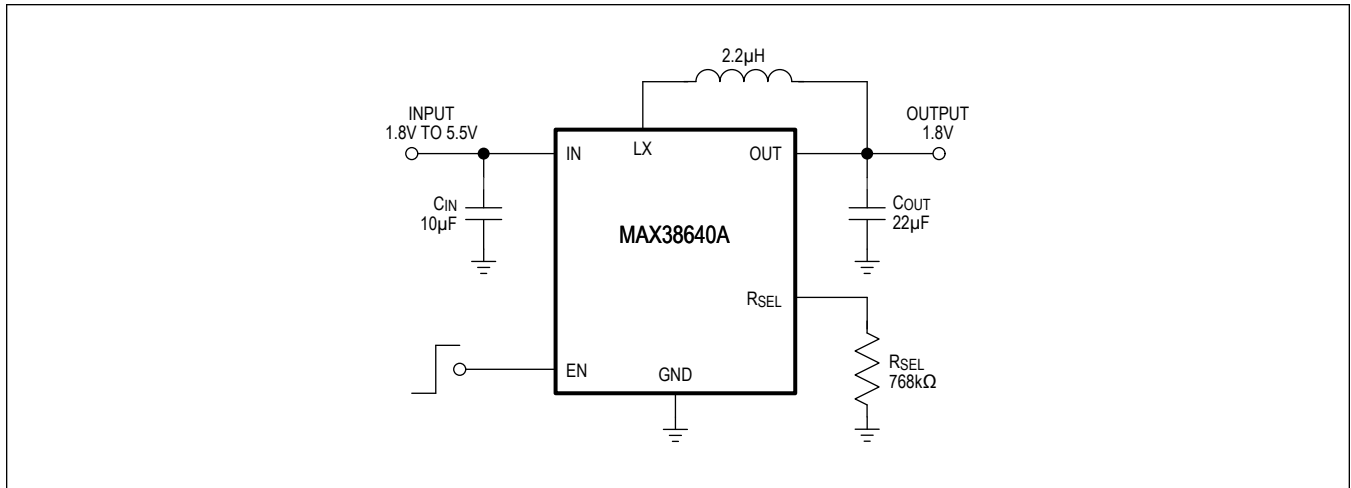
- Extends Battery Life
 - 330nA Ultra-Low Quiescent Supply Current
 - 5nA Shutdown Current
 - 96% Peak Efficiency and over 88% at 10 μ A
- Easy to Use – Addresses Popular Operation
 - 1.8V to 5.5V Input Range
 - Single Resistor-Adjustable V_{OUT} from 0.7V to 3.3V (A-Option)
 - Preprogrammed V_{OUT} from 0.5V to 5.0V (B-Option)
 - \pm 1.75% Output Voltage Accuracy
 - Up to 175mA/350mA/700mA Load Current
- Protects System in Multiple Use Cases
 - Reverse-Current Blocking in Shutdown
 - Optional Active Discharge Feature
- Reduces Size and Increases Reliability
 - -40°C to +85°C Temperature Range
 - 2mm x 2mm, 6-pin μ DFN Package
 - 1.42mm x 0.89mm, 0.4mm Pitch 6-pin (2 x 3) WLP

[Ordering Information](#) appears at end of data sheet.

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Typical Operating Circuit



Absolute Maximum Ratings

| | | | |
|---|--|--|-----------------|
| IN, EN, R _{SEL} , NC, OUT to GND..... | -0.3V to +6V | Operating Temperature Range | -40°C to +85°C |
| LX RMS Current WLP | -1.6A _{RMS} to +1.6A _{RMS} | Maximum Junction Temperature | +150°C |
| LX RMS Current μ DFN..... | -1A _{RMS} to +1A _{RMS} | Storage Temperature Range | -65°C to +150°C |
| Continuous Power Dissipation—WLP (T _A = +70°C) (Derate 10.5mW/°C above +70°C)..... | 840mW | Lead Temperature (soldering, 10 seconds) | +300°C |
| Continuous Power Dissipation— μ DFN (T _A = +70°C) (Derate 4.5mW/°C above +70°C)..... | 357.8mW | Soldering Temperature (reflow) | +260°C |

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Package Information

6 μ DFN

| | |
|--|-------------------------|
| Package Code | L622+1C |
| Outline Number | 21-0164 |
| Land Pattern Number | 90-0004 |
| Thermal Resistance, Four-Layer Board: | |
| Junction to Ambient (θ_{JA}) | 223.6°C/W |
| Junction to Case (θ_{JC}) | 122°C/W |

6 WLP

| | |
|--|--|
| Package Code | N60E1+2 |
| Outline Number | 21-100128 |
| Land Pattern Number | Refer to Application Note 1891 |
| Thermal Resistance, Four-Layer Board: | |
| Junction-to-Ambient (θ_{JA}) | 95.15°C/W |

For the latest package outline information and land patterns (footprints), go to www.maximintegrated.com/packages. Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

Package thermal resistances were obtained using the method described in JEDEC specification JESD51-7, using a four-layer board. For detailed information on package thermal considerations, refer to www.maximintegrated.com/thermal-tutorial.

Electrical Characteristics

(V_{IN} = 3.3V, V_{OUT} = 1.8V, T_A = -40°C to +85°C, C_{IN} = 4.7μF, C_{OUT} = 10μF, unless otherwise specified. (*Note 1*))

| PARAMETER | SYMBOL | CONDITIONS | | MIN | TYP | MAX | UNITS |
|---|------------------------|---|------------------------|-------|-------|-------|-------|
| Shutdown Current | I _{IN_SD} | V _{EN} = 0V, T _A = +25°C | | | 0.005 | 0.1 | μA |
| Input Voltage Range | V _{IN_RANGE} | Guaranteed by output accuracy | | 1.8 | | 5.5 | V |
| Input Undervoltage Lockout | V _{UVLO} | R _{SEL} > 50kΩ (MAX38640A/1A/ 2A/3A), (MAX38640B/1B/ 2B/3B) | V _{IN} rising | | 1.75 | 1.8 | V |
| | | | Hysteresis | | 50 | | mV |
| | V _{UVLO} | R _{SEL} < 50kΩ (MAX38640A/1A/ 2A/3A) | V _{IN} rising | | 2.6 | 2.7 | V |
| | | | Hysteresis | | 125 | | mV |
| Output Voltage Range | V _{OUT_RANGE} | Guaranteed by output accuracy | | 0.5 | | 5 | V |
| Output Accuracy | V _{OUT_ACC} | OUT falling, when LX begins switching above 1MHz, V _{OUT} = 0.7V to 3.3V, V _{IN} = 5.5V (<i>Note 2</i>) | | -1.75 | | +1.75 | % |
| DC Line Regulation | V _{LREG} | V _{OUT} = 1.8V, V _{IN} = 2.0V to 5.5V, I _{OUT} = 10mA to 160mA | | | ±1.5 | | % |
| Quiescent Supply Current into IN | I _{Q_IN} | V _{EN} = V _{IN} , not switching V _{OUT} = 106% of target voltage, V _{OUT TARGET} = 2.5V, T _A = +25°C | | | 330 | 660 | nA |
| Quiescent Supply Current into OUT | I _{Q_OUT} | V _{EN} = V _{IN} , not switching V _{OUT} = 106% of target voltage, V _{OUT TARGET} = 2.5V, T _A = +25°C | | | 10 | | nA |
| Soft-Start Slew Rate | dV _{OUT} /dt | V _{OUT} = 1.8V, no load | | | 6.5 | | mV/μs |
| LX Leakage Current | I _{LEAK_LX} | V _{LX} = V _{IN} = 5.5V, T _A = +25°C | | | 2 | 100 | nA |
| Inductor Peak Current Limit | I _{PEAK_LX} | MAX38643 | | 800 | 1000 | 1200 | mA |
| | | MAX38641/MAX38642 | | 400 | 500 | 600 | |
| | | MAX38640 | | 225 | 250 | 300 | |
| High-Side R _{DS(on)} | R _{DS_H} | V _{IN} = 3.3V | MAX38643 | | 95 | 150 | mΩ |
| | | | MAX38641/ MAX38642 | | 170 | 320 | |
| | | | MAX38640 | | 320 | 600 | |
| Low-Side R _{DS(on)} | R _{DS_L} | V _{IN} = 3.3V | MAX38643 | | 50 | 90 | mΩ |
| | | | MAX38641/ MAX38642 | | 80 | 160 | |
| | | | MAX38640 | | 150 | 290 | |
| Zero-Crossing Threshold | I _{ZX_LX} | V _{OUT} = 1.2V, percent of I _{PEAK_LX} | | | 5 | | % |
| Enable Input Leakage | I _{LEAK_EN} | V _{EN} = 5.5V, T _A = +25°C | | | 0.1 | 100 | nA |
| Enable Voltage Threshold | V _{EN_R} | V _{EN} rising | | | 0.8 | 1.2 | V |
| | V _{EN_F} | V _{EN} falling | | 0.4 | 0.7 | | |
| Active Discharge Resistance (MAX38640/1/3 Only) | R _{OUT_DIS} | V _{EN} = 0V | | 50 | 85 | 200 | Ω |

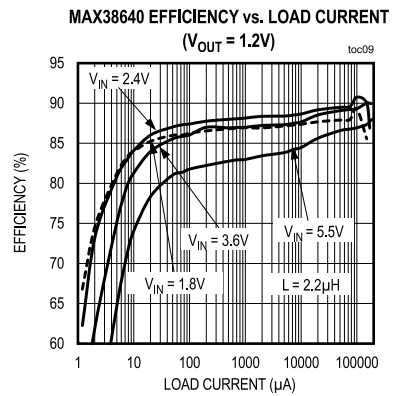
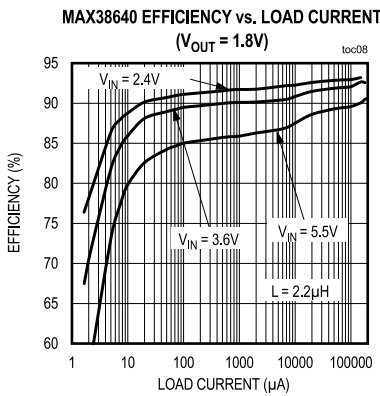
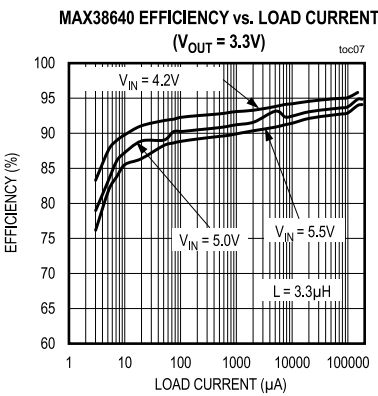
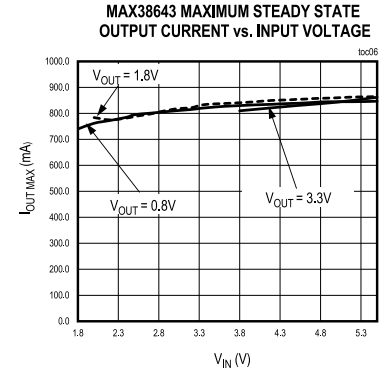
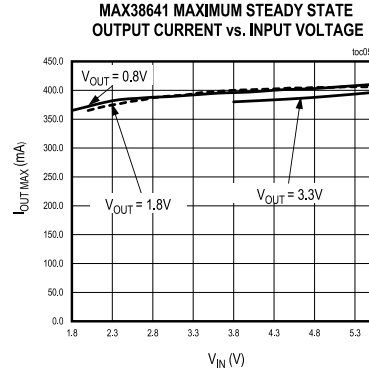
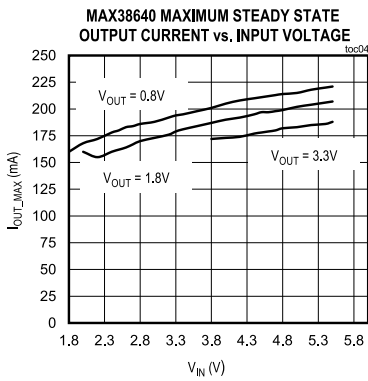
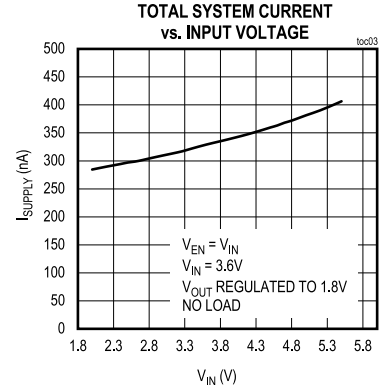
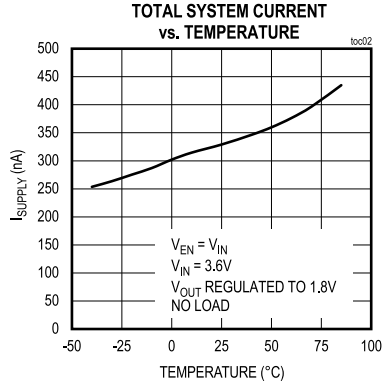
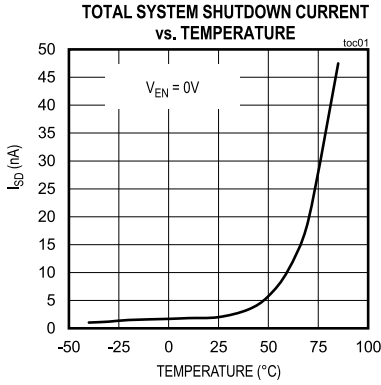
Electrical Characteristics (continued)(V_{IN} = 3.3V, V_{OUT} = 1.8V, T_A = -40°C to +85°C, C_{IN} = 4.7μF, C_{OUT} = 10μF, unless otherwise specified. (*Note 1*))

| PARAMETER | SYMBOL | CONDITIONS | MIN | TYP | MAX | UNITS |
|---|-------------------|---|-----|-----|------|-------|
| Required Select Resistor Accuracy (MAX38640A/1A/2A/3A Only) | R _{SEL} | Use the nearest ±1% resistor from Table 1 | -1 | | +1 | % |
| Select Resistor Detection Time (MAX38640A/1A/2A/3A Only) | t _{RSEL} | C _{SEL} < 2pF | 240 | 600 | 1320 | μs |
| Thermal Shutdown | T _{SHUT} | T _J rising when output turns off | | 165 | | °C |
| Thermal Shutdown Threshold | T _{SHUT} | T _J falling when output turns on | | 150 | | °C |

Note 1: Limits over the specified operating temperature and supply voltage range are guaranteed by design and characterization, and production tested at room temperature only.**Note 2:** Output Accuracy in low-power mode (LPM) and does not include load, line, or ripple.

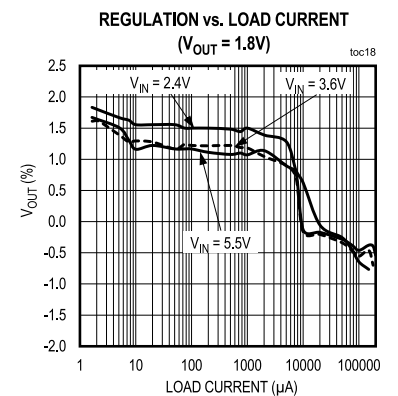
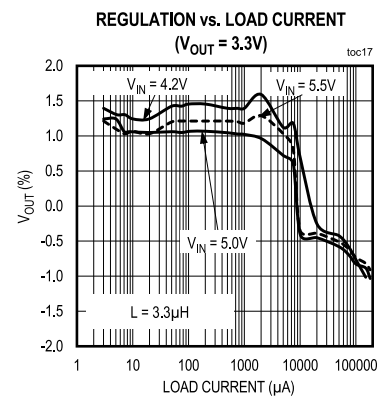
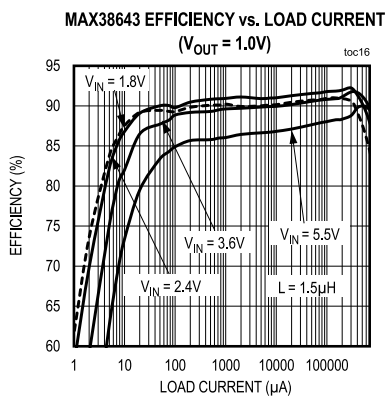
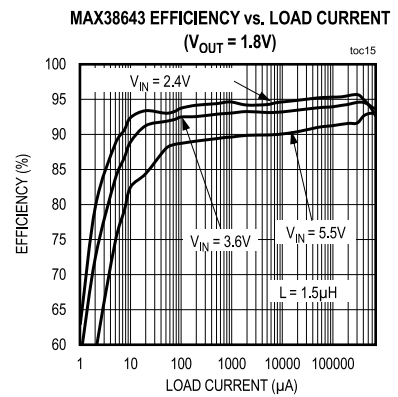
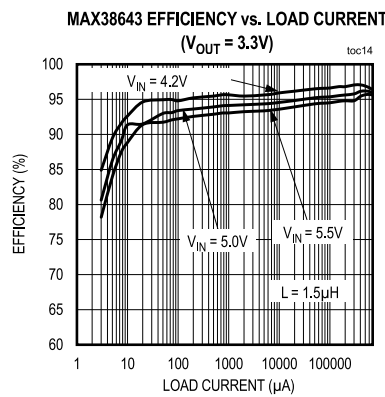
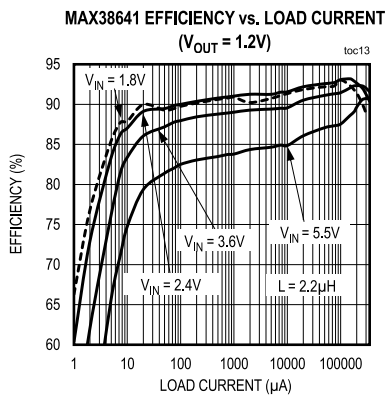
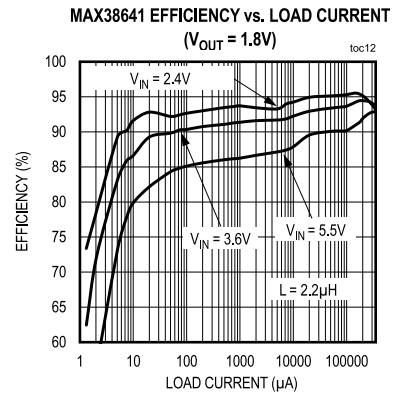
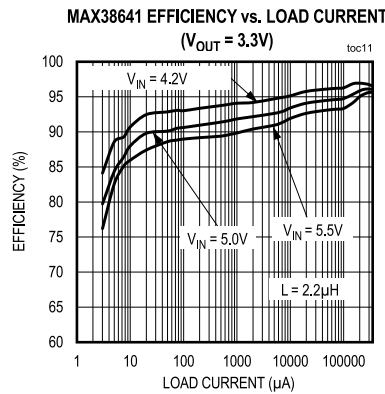
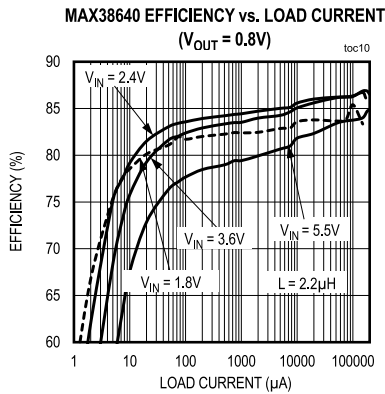
Typical Operating Characteristics

(MAX38640AENT+, V_{IN} = 3.6V, V_{OUT} = 1.8V, L = 2.2μH, C_{IN} = 10μF, C_{OUT} = 22μF, T_A = +25°C, unless otherwise noted.)



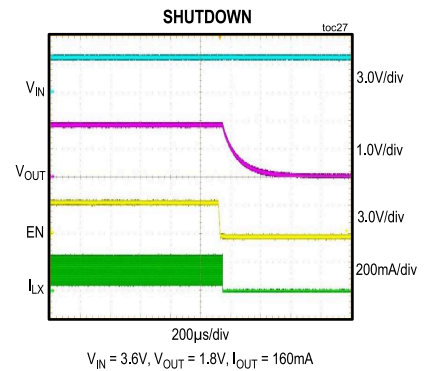
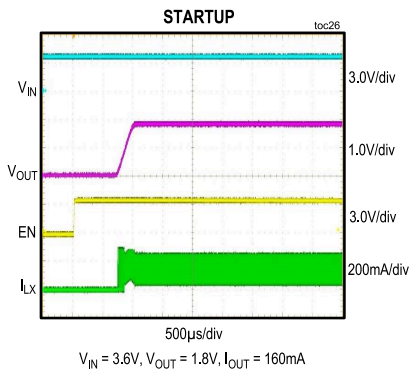
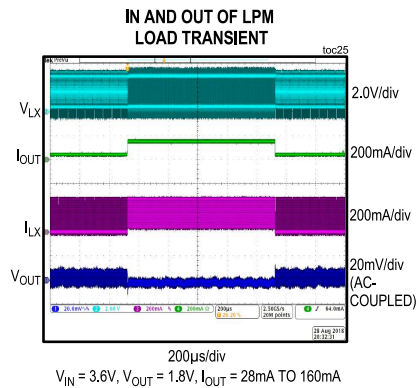
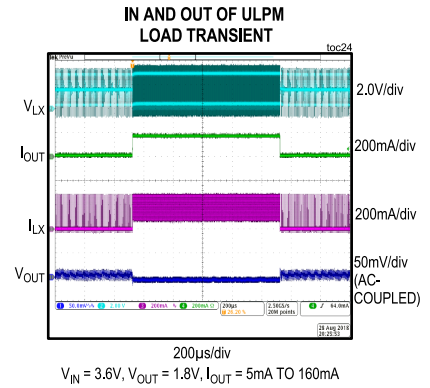
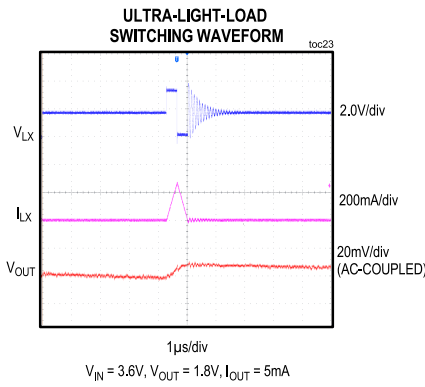
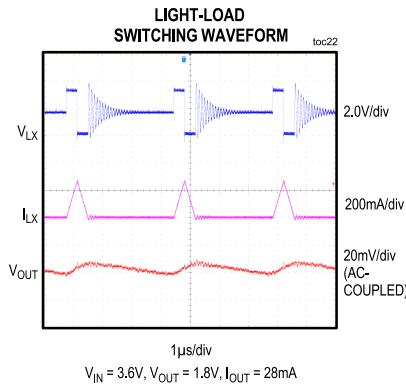
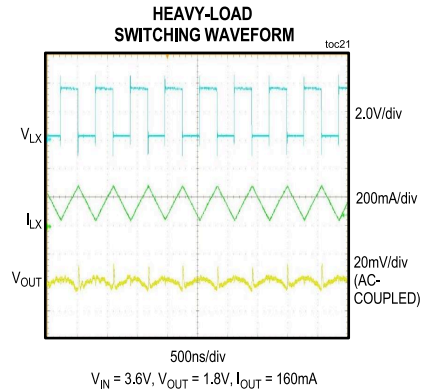
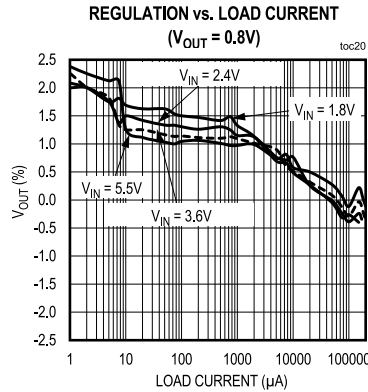
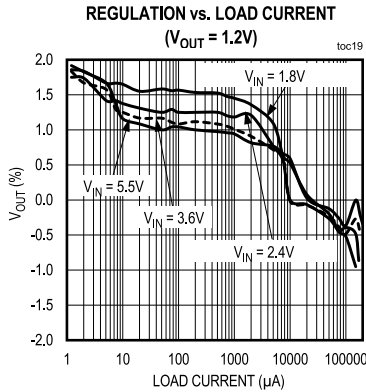
Typical Operating Characteristics (continued)

(MAX38640AENT+, V_{IN} = 3.6V, V_{OUT} = 1.8V, L = 2.2μH, C_{IN} = 10μF, C_{OUT} = 22μF, T_A = +25°C, unless otherwise noted.)



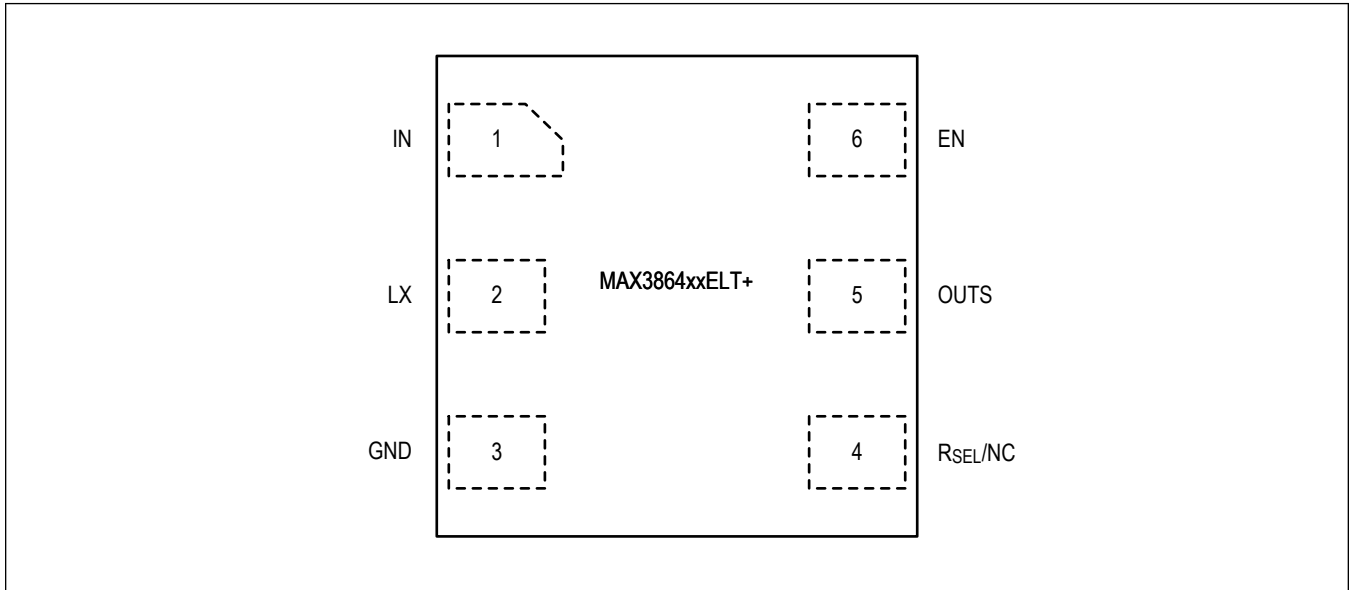
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(MAX38640AENT+, V_{IN} = 3.6V, V_{OUT} = 1.8V, L = 2.2μH, C_{IN} = 10μF, C_{OUT} = 22μF, T_A = +25°C, unless otherwise noted.)

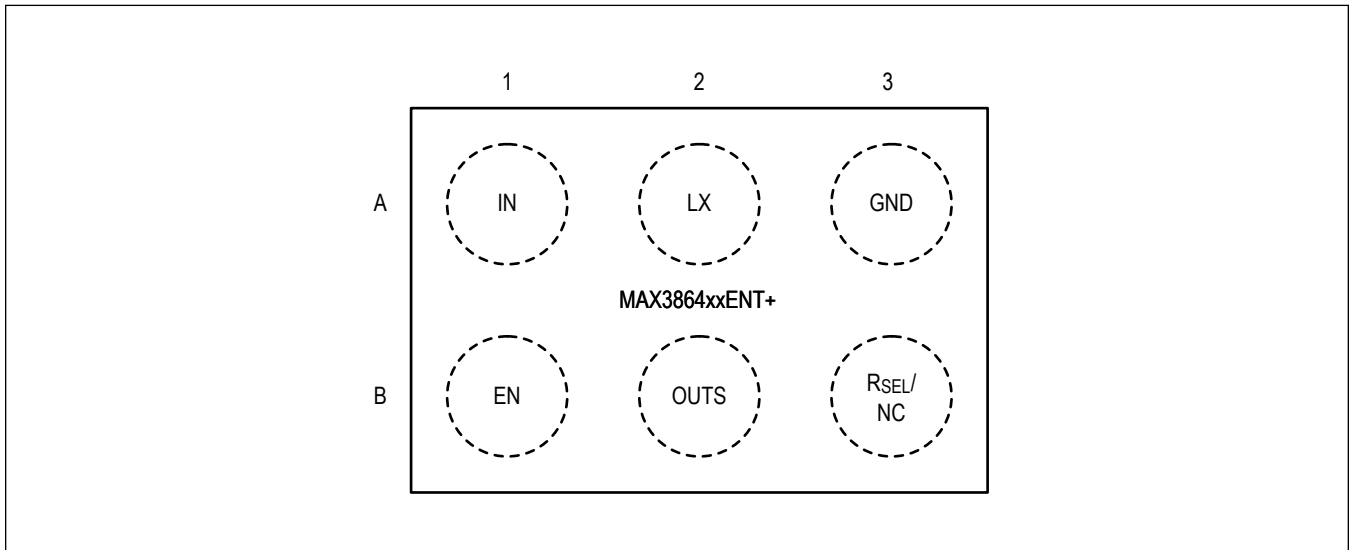


Pin Configurations

6 μ DFN



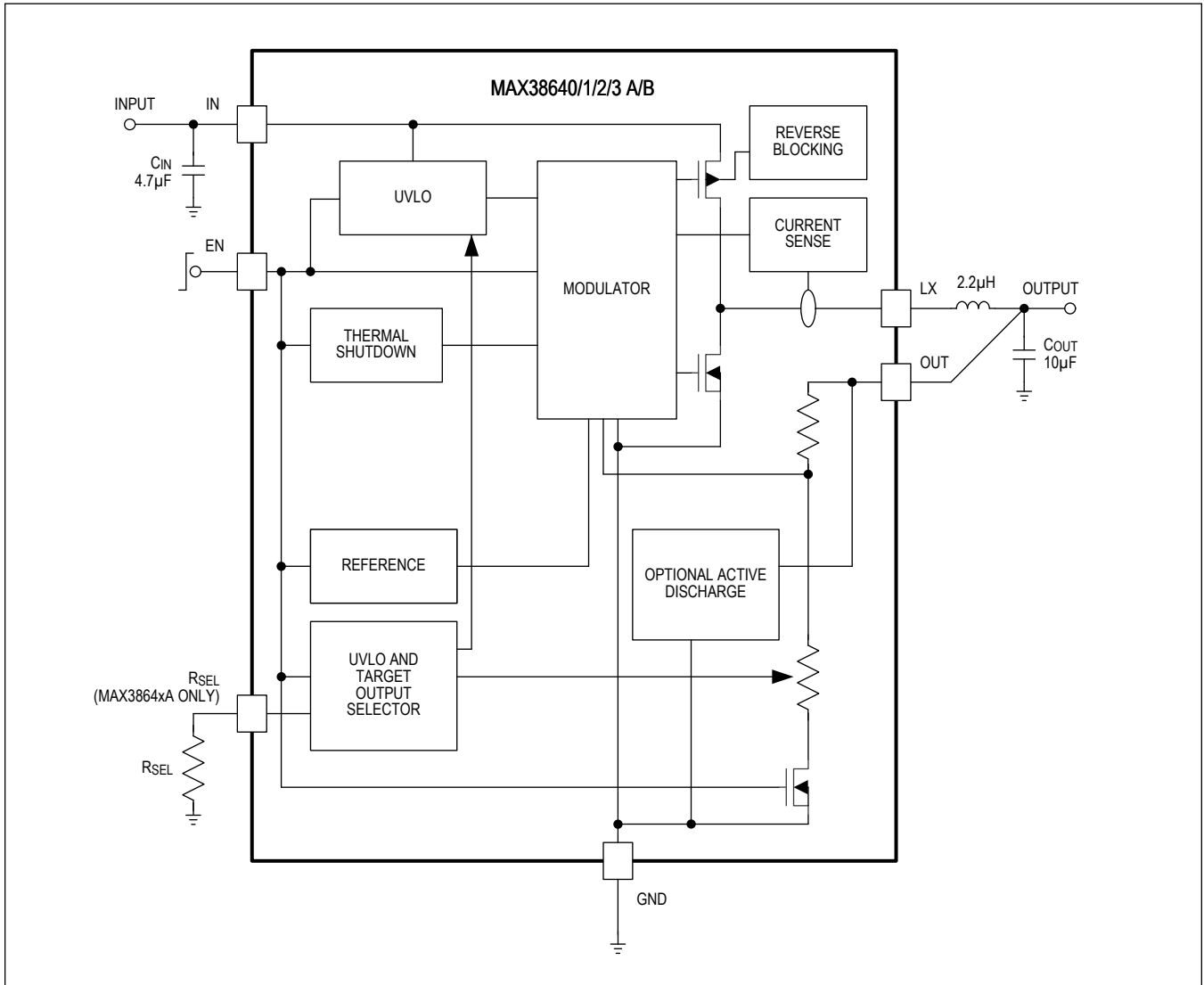
6 WLP



Pin Descriptions

| PIN | | NAME | FUNCTION |
|-------------------|-------------------|----------------------|---|
| MAX3864xxE LT+ | MAX3864xxE NT+ | | |
| 1 | A1 | IN | Regulator Supply Input. Connect to a voltage between 1.8V and 5.5V and bypass with a ceramic capacitor from IN to GND. |
| 2 | A2 | LX | Switching Node. Connect an inductor between LX and the regulator output. |
| 3 | A3 | GND | Ground. |
| 4 | B3 | R _{SEL} /NC | <p>MAX38640A/1A/2A/3A: Output Voltage Select Input. Connect a resistor from R_{SEL} to GND to program the output voltage and IN undervoltage threshold based on Table 1.</p> <p>MAX38640B/1B/2B/3B: No Connect. Leave floating.</p> |
| 5 | B2 | OUT | Output Voltage Sense Input. Connect to the load at a point where accurate regulation (output capacitor) is required to eliminate resistive metal drops. |
| 6 | B1 | EN | Enable Input. Force this pin high to enable the buck converter. Force this pin low to disable the part and enter shutdown. |

Functional Diagram



Detailed Description

The MAX38640–MAX38643 are ultra-low I_Q (330nA) buck converters that step down from 1.8V to 5.5V to wide range of output voltages between 0.5V and 5V. The output voltage is either programmable on the MAX38640A/1A/2A/3A versions using an external resistor or fixed for the MAX38640B/1B/2B/3B versions. The external R_{SEL} resistor on the R_{SEL} pin programs the output voltage upon startup for the MAX38640A/1A/2A/3A versions.

The buck converter automatically switches between ultra-low-power mode (ULPM), low-power mode (LPM), and high-power mode (HPM) to better service the load, depending on the load current. The buck converter overregulates in ultra-low-power mode to allow the output capacitor to handle the transient load currents. The device has 90% duty cycle limitation.

Active discharge resistor in the MAX38640/MAX38641/MAX38643 pulls OUT to ground when the part is in shutdown. Active discharge has been strategically omitted for the MAX38642 to preserve the charge on the output capacitor in shutdown. Harvesting applications where the output is connected to a supercapacitor can take advantage of reverse-current blocking feature to preserve the charge on the output capacitor even if the input were to fall below the output in shutdown. Applications where two MAX38642 buck converters are connected in parallel to drive the load can have the input of one of the buck converters to go to 0V in shutdown without dragging the output down or loading the other buck.

Voltage Configuration

The MAX38640A/1A/2A/3A includes an R_{SEL} pin to configure the output voltage and input UVLO threshold on startup. Resistors with tolerance 1% (or better) should be chosen, with nominal values specified in [Table 1](#).

Table 1. MAX38640A/1A/2A/3A R_{SEL} Selection Table

| TARGET OUTPUT VOLTAGE (V) | R _{SEL} (kΩ) | INPUT UVLO THRESHOLD, RISING (V) |
|---------------------------|-----------------------|----------------------------------|
| 2.5 | OPEN | 1.75 |
| 2 | 909 | 1.75 |
| 1.8 | 768 | 1.75 |
| 1.5 | 634 | 1.75 |
| 1.3 | 536 | 1.75 |
| 1.25 | 452 | 1.75 |
| 1.2 | 383 | 1.75 |
| 1.15 | 324 | 1.75 |
| 1.1 | 267 | 1.75 |
| 1.05 | 226 | 1.75 |
| 1 | 191 | 1.75 |
| 0.95 | 162 | 1.75 |
| 0.9 | 133 | 1.75 |
| 0.85 | 113 | 1.75 |
| 0.8 | 95.3 | 1.75 |
| 0.75 | 80.6 | 1.75 |
| 0.7 | 66.5 | 1.75 |
| 3.3 | 56.2 | 2.6 |
| 3 | 47.5 | 2.6 |

Table 1. MAX38640A/1A/2A/3A R_{SEL} Selection Table (continued)

| TARGET OUTPUT VOLTAGE (V) | R _{SEL} (kΩ) | INPUT UVLO THRESHOLD, RISING (V) |
|---------------------------|-----------------------|----------------------------------|
| 2.8 | 40.2 | 2.6 |
| 2.75 | 34 | 2.6 |
| 2.5 | 28 | 2.6 |
| 2 | 23.7 | 2.6 |
| 1.8 | 20 | 2.6 |
| 1.5 | 16.9 | 2.6 |
| 1.25 | 14 | 2.6 |
| 1.2 | 11.8 | 2.6 |
| 1.15 | 10 | 2.6 |
| 1.1 | 8.45 | 2.6 |
| 1 | 7.15 | 2.6 |
| 0.95 | 5.9 | 2.6 |
| 0.9 | 4.99 | 2.6 |
| 0.8 | SHORT TO GND | 2.6 |

The MAX38640B/1B/2B/3B has a fixed output voltage that is preprogrammed (no R_{SEL} programming). Contact Maxim to order a part with other preprogrammed output voltage selections. The input UVLO threshold for MAX38640B/1B/2B/3B is 1.75V (typ, V_{IN} rising) with 50mV hysteresis (typ).

Applications Information

Inductor Selection

The inductor value for the MAX38640–MAX38643 affects the ripple current, the transition point from low-power mode (LPM) to ultra-low-power mode (ULPM), and the overall efficiency performance. Based on the peak current limit required for different applications, it is recommended to select an inductor value based on [Table 2](#).

Table 2. Inductor Selection

| PEAK CURRENT, PART NUMBER | INDUCTANCE RANGE (μH) |
|---------------------------|------------------------------------|
| 1.0A, MAX38643 | 1.0 to 1.5 |
| 500mA, MAX38641/MAX38642 | 2.2 |
| 250mA, MAX38640 | 2.2 to 4.7 |

Input Capacitor

The input capacitor (C_{IN}) reduces the peak current drawn from the battery or input power source and reduces the switching noise in the IC. The impedance of C_{IN} at the switching frequency should be very low. Ceramic capacitors are recommended with their small size and low ESR. For most MAX38640 applications, use a 10 μF ceramic capacitor with X5R or X7R temperature characteristics.

For MAX38641–MAX38643 applications, using a 22 μF ceramic input capacitor is recommended. When operating at V_{IN} close to the UVLO, more input capacitance may be required to keep the input voltage ripple from tripping the UVLO protection.

Output Capacitor

The output capacitor (C_{OUT}) is required to keep the output voltage ripple small and to ensure loop stability. C_{OUT} must have low impedance at the switching frequency. Ceramic capacitors are recommended due to their small size and low ESR. Make sure the capacitor does not degrade its capacitance significantly over temperature and DC bias. Capacitors with X5R or X7R temperature characteristics typically perform well. A 22 μF ceramic capacitor is recommended for most MAX38640 and MAX38641/MAX38642 applications. In case of low V_{OUT} target levels when using the MAX38641/MAX38642, two 22 μF output capacitors are recommended. For MAX38643 applications, use two 22 μF output capacitors.

Enabling Device

The device has a dedicated EN pin. This pin can be driven by a digital signal. It is recommended that the digital signal enables the device after V_{IN} crosses the UVLO threshold. In applications where EN is tied to IN, the device is designed to be powered by fast V_{IN} slew rates. If V_{IN} slew rates are slower than 5V/ms, users must delay enabling the device until after V_{IN} crosses the UVLO threshold. This can be done using a simple RC circuit, as shown in [Figure 1](#).

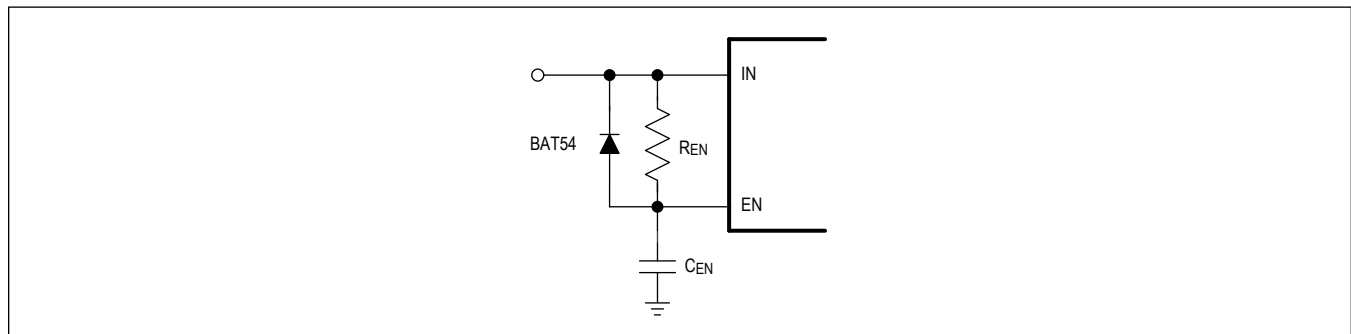


Figure 1. RC Circuit at EN

PCB Layout and Routing

High switching frequencies and large peak currents make PCB layout a very important part of the buck regulator design. Good design minimizes excessive electromagnetic interference (EMI) on the feedback paths and voltage gradients in the ground plane to avoid instability and regulation errors. The input capacitor (C_{IN}) should be placed as close as possible to the IC pins IN and GND. Connect the inductor, C_{IN}, and output capacitor (C_{OUT}) as close together as possible, and keep their traces short, direct, and wide.

Connect the two GND pins under the IC and directly to the ground of C_{OUT}. Keep noisy traces, such as the LX node, as short as possible. The OUT pin should be connected to the output capacitor and this trace should be routed away from the main power path between the inductor and C_{OUT}. The OUT trace should also be routed away from noisy traces such as the LX line or other external noise sources. Refer to the MAX38640–MAX38643 evaluation kit for an example PCB layout and routing scheme.

Ordering Information

| PART NUMBER | OUTPUT CURRENT (mA) | ACTIVE DISCHARGE | FEATURES | PACKAGE |
|-------------------|---------------------|------------------|--|----------------------------|
| MAX38640AELT+ | 175 | Yes | 0.7V to 3.3V Resistor-Selectable Output Voltage Using R _{SEL} Pin | 6-pin, 2mm x 2mm μ DFN |
| MAX38641AELT+ | 350 | Yes | | |
| MAX38642AELT+ | 350 | No | | |
| MAX38643AELT+ | 700 | Yes | | |
| MAX38640BELT+* | 175 | Yes | 0.5V to 5V Preprogrammed Output Voltage | |
| MAX38641BELT+* | 350 | Yes | | |
| MAX38642BELT+* | 350 | No | | |
| MAX38643BELT+* | 700 | Yes | | |
| MAX38640BELT18+T | 175 | Yes | 1.8V Preprogrammed Output Voltage | |
| MAX38640AENT+ | 175 | Yes | 0.7V to 3.3V Resistor-Selectable Output Voltage Using R _{SEL} Pin | |
| MAX38641AENT+ | 350 | Yes | | |
| MAX38642AENT+ | 350 | No | | |
| MAX38643AENT+ | 700 | Yes | | |
| MAX38640BENT+* | 175 | Yes | 0.5V to 5V Preprogrammed Output Voltage | |
| MAX38641BENT+* | 350 | Yes | | |
| MAX38642BENT+* | 350 | No | | |
| MAX38643BENT+* | 700 | Yes | | |
| MAX38640BENT06+T | 175 | Yes | 0.6V Preprogrammed Output Voltage | |
| MAX38640BENT21+T | 175 | Yes | 2.1V Preprogrammed Output Voltage | |
| MAX38643BENT185+T | 700 | Yes | 1.85V Preprogrammed Output Voltage | |
| MAX38641BENT12+T | 350 | Yes | 1.2V Preprogrammed Output Voltage | |
| MAX38640BENT18+T | 175 | Yes | 1.8V Preprogrammed Output Voltage | |

*Future product—contact factory for availability.

+Denotes a lead(Pb)-free/RoHS-compliant package.

T = tape-and-reel.

Revision History

| REVISION NUMBER | REVISION DATE | DESCRIPTION | PAGES CHANGED |
|-----------------|---------------|--|--------------------|
| 0 | 10/18 | Initial release | — |
| 1 | 1/19 | Updated <i>Typical Operating Characteristics</i> , <i>Pin Descriptions</i> , <i>Functional Diagram</i> , <i>Applications Information</i> , and <i>Ordering Information</i> | 5, 6, 8, 9, 13, 14 |
| 2 | 5/19 | Updated title of data sheet and changed part number references, updated <i>Typical Operating Characteristics</i> | 1, 3–5, 7, 9–15 |
| 3 | 4/20 | Updated <i>Ordering Information</i> | 14 |
| 4 | 9/20 | Updated <i>Ordering Information</i> | 14 |
| 5 | 3/21 | Removed <i>Table 2</i> , updated <i>Ordering Information</i> | 12, 14 |

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