

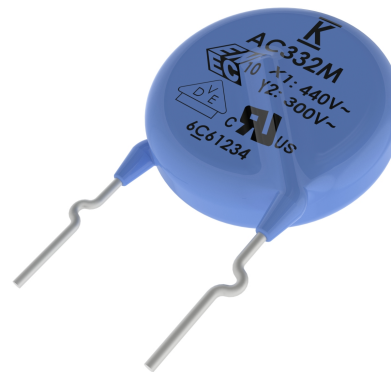
## Safety Standard Recognized, 900 Series, Encapsulated, AC Type, X1 440 VAC/Y2 300 VAC (Industrial Grade)

### Overview

KEMET's 900 Series encapsulated radial leaded ceramic disc capacitors are specifically designed for interference-suppression AC line filtering applications. Having internationally recognized safety certifications, these capacitors are well-suited for applications that require keeping potentially disruptive or damaging line transients and EMI out of susceptible equipment. They are also an ideal solution in situations where there is a need to suppress line disturbances at the source.

Safety Certified Capacitors are classified as either X and/or Y capacitors. Class X capacitors are primarily used in line-to-line (across-the-line) applications. In this application there is no danger of electric shock to humans should the capacitor fail, but could result in a risk of fire. The class Y capacitor is primarily used in line-to-ground (line by-pass) applications. In this application, failure of the capacitor could lead to danger of electric shock.

With a working voltage of 440 VAC in line-to-line (Class X) and 300 VAC in line-to-ground (Class Y) applications, these safety capacitors meet the impulse test criteria outlined in IEC Standard 60384. Meeting subclass X1 and Y2 requirements, these devices are certified to withstand impulses up to 4 KV (X1) and 5 KV (Y2) respectively. These encapsulated devices also meet the flame test requirements outlined in UL Standard 94V-0.



### Ordering Information

C9	7	1	U	472	M	Z	W	D	A	A	7317
Ceramic Series	Body Diameter	Lead Spacing <sup>1,2,4</sup>	Spec.	Capacitance Code (pF)	Capacitance Tolerance	Rated Voltage	Dielectric/Temp. Char.	Design	Lead Config. <sup>1,3,4</sup>	Failure Rate	Packaging (C-Spec)
C9 = Ceramic 900 Series	0 = 7.0 mm 1 = 8.0 mm 2 = 9.0 mm 3 = 10.0 mm 4 = 11.0 mm 6 = 13.0 mm 8 = 15.0 mm	7 = 7.5 mm 1 = 10.0 mm	U = Safety	Two significant digits and number of zeroes	J = ±5% K = ±10% M = ±20%	Z = X1 440 VAC /Y2 300 VAC	S = SL Y = Y5P W = Y5U V = Y5V	D = Disc	A = Straight B = Vertical Kink C = Outside Kink D = Inside Kink	A = N/A	See "Packaging C-Spec Ordering Options Table" below

<sup>1</sup> Due to a high risk of arcing, "Inside Kink" lead configuration cannot be combined with the 5 mm lead spacing option. The "Inside Kink" option is only available on capacitors with lead spacing of 7.5 mm or greater. A potential for arcing may exist when combining the "Inside Kink" lead configuration with a 7.5mm lead spacing option, especially in high humidity environments and/or when exposure to voltages and transients may impact creepage and clearance requirements.

<sup>2</sup> Capacitor body diameter will limit available lead spacing and packaging options. See "Dimensions" and "Product Ordering Codes and Ratings" sections of this document to determine availability.

<sup>3</sup> "Vertical Kink", "Outside Kink" and "Inside Kink" lead configurations cannot be combined with the bulk/20 mm lead length option (WL20). 20 mm lead length is only available on capacitors with straight leads (lead configuration ordering code "A"). For nonstandard lead length inquiries, please contact KEMET.

<sup>4</sup> Bulk packaging lead length availability is dependent upon "Lead Configuration" and "Lead Spacing." See "Dimensions" section of this document to verify availability of a specific lead length option. For nonstandard lead length inquiries, please contact KEMET.

## Packaging C-Spec Ordering Options Table

Packaging Type	Lead Length (mm) <sup>2,3</sup>	Packaging Ordering Code (C-Spec)
Ammo Pack	20.0+1.5/-1.0 (straight leads) 18.0+2.0/-0 (preformed leads <sup>1</sup> )	7317
Bulk Bag	3.0±1.0	WL30
	3.5±1.0	WL35
	4.0±1.0	WL40
	4.5±1.0	WL45
	5.0±1.0	WL50
	20.0 minimum <sup>4</sup>	WL20

<sup>1</sup> Preformed (crimped) lead configurations include vertical kink, outside kink and inside kink. See "Lead Configurations" and "Ordering Information" sections of this document for further details.

<sup>2</sup> "Vertical Kink", "Outside Kink" and "Inside Kink" lead configurations cannot be combined with the bulk/20 mm lead length option (WL20). 20 mm lead length is only available on capacitors with straight leads (lead configuration ordering code "A"). For nonstandard lead length inquiries, please contact KEMET.

<sup>3</sup> For nonstandard lead length inquiries, please contact KEMET.

<sup>4</sup> Lead length of 20.0 mm minimum only available for straight leads.

## Benefits

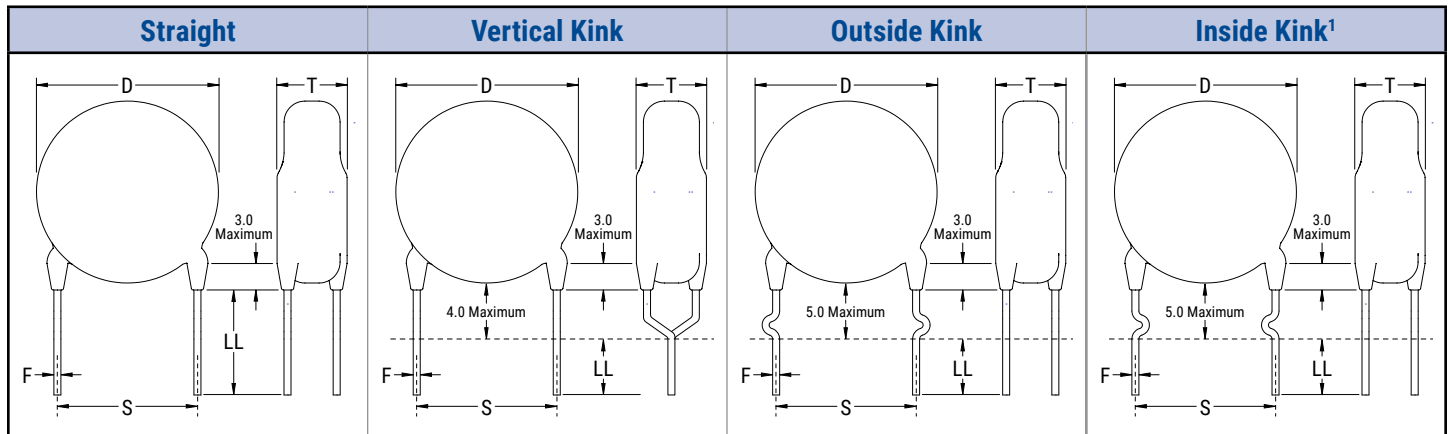
- Safety Standard Recognized (IEC 60384-14)
- Reliable operation up to 125°C
- Class X1/Y2
- 7.5 mm and 10 mm lead spacing
- Lead (Pb)-free and RoHS Compliant
- Halogen free
- Capacitance offerings ranging from 10 pF up to 10 nF
- Available capacitance tolerances of ±5%, ±10% and ±20%
- High reliability
- Preformed (crimped) or straight lead configurations
- Non-polar device, minimizing installation concerns
- Encapsulation meets flammability standard UL 94V-0

## Applications

Typical applications include:

- Line-to-line (Class X) filtering
- Line-to-ground (Class Y) filtering
- Antenna coupling
- Primary and secondary coupling (switching power supplies)
- Line disturbances suppression (motors and motor controls, relays, switching power supplies, and inverters)

## Lead Configurations



<sup>1</sup> Due to a high risk of arcing, the "Inside Kink" lead configuration option cannot be combined with 5 mm lead spacing ("S" dimension above). The "Inside Kink" option is only available on devices with lead spacing of 7.5 mm or 10 mm.

## Dimensions – Millimeters

Lead Configuration	Lead Configuration Ordering Code <sup>1</sup>	S	Lead Spacing Tolerance	D	T	e	ØF
		Lead Spacing <sup>2</sup>		Body Diameter <sup>2</sup>	Body Thickness	Lead Meniscus	Lead Diameter
Straight	A	7.5	±1.0	See Table 1 - "Product Ordering Codes and Ratings"	3.0 maximum	0.55±0.1	
		10.0	±1.0				
Vertical Kink (Preformed)	B	7.5	±1.0				
		10.0	±1.0				
Outside Kink (Preformed)	C	7.5	±1.0				
		10.0	±1.0				
Inside Kink (Preformed)	D	7.5	±1.0				
		10.0	±1.0				

<sup>1</sup> Lead Configuration is identified in the 13th character of the ordering code. See "Lead Configuration" and "Ordering Information" sections of this document for further details.

<sup>2</sup> Body diameter of capacitor will limit available lead spacing and packaging options. See "Product Ordering Codes and Ratings" sections of this document for further details.

## Approval Standard and Certification No.

Safety Standard	Standard No.	Subclass	Working Voltage	Certificate No.
VDE (ENEC)	IEC 60384-14	X1	440 VAC	40036415
		Y2	300 VAC	
UL CAN/CSA	UL 60384-14 and E60384-14	X1	440 VAC	E356389
		Y2	300 VAC	

These devices are VDE/ENEC and UL recognized for antenna coupling and AC line-to-line (Class X) and line-to-ground (Class Y) applications per IEC60384-14 and UL 60384-14.

## Environmental Compliance

These devices are Halogen free and RoHS Compliant. They meet all requirements set forth by both EU and China RoHS directives.



## General Specifications/Performance Characteristics

Dielectric/Temperature Characteristic	SL	Y5P	Y5U	Y5V
Operating Temperature Range:	-40°C to +125°C			
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC):	-1,000 ~ +350 ppm/°C	±10%	+20%/-55%	~ +30%/-80%
Dielectric Withstanding Voltage (7.5 mm and 10 mm Lead Spacing)	2,600 VAC (60±5 seconds at 25°C)			
Quality Factor (Q)	30 pF and above: ≥ 1,000 Below 30 pF: ≥ 400 +(20x)*	See "Dissipation Factor"		
Dissipation Factor (tanδ) at +25°C <sup>1</sup>	See "Quality Factor"	2.50%	2.50%	5.0%
Insulation Resistance (IR) Limit at +25°C	10,000 MΩ Minimum (500 VDC applied for 60±5 seconds at 25°C)			

\*C = Nominal capacitance

<sup>1</sup> Capacitance and Dissipation Factor (DF) measured under the following conditions:

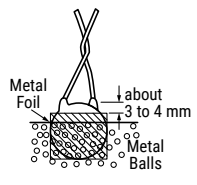
SL: 1 MHz ± 100 kHz and 1.0 ± 0.2 Vrms

X5P, Y5U and Y5V: 1 kHz ± 50 Hz and 1.0 ± 0.2 Vrms

Note: When measuring capacitance, it is important to ensure the set voltage level is held constant. The HP4284 and Agilent E4980 have a feature known as Automatic Level Control (ALC). The ALC feature should be switched to "ON."

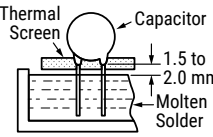
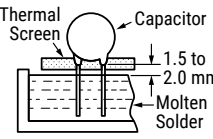


**Table 2 – Performance & Reliability: Test Methods and Conditions**

Item		Specification	Test Method																					
Operating Temperature Range		-40°C to +125°C																						
Dielectric Strength	Between lead wires	No failures	The capacitor shall not be damaged when 2,600 VAC(rms) is applied between the lead wires for 60 seconds.																					
	Body Insulation	No failures	<p>The terminals (leads) of the capacitor shall be connected together. A metal foil is tightly wrapped around the body of the capacitor at a distance of about 3 to 4 mm from each terminal. The capacitor is then inserted into a container filled with metal balls approximately 1 mm in diameter. 2,600 VAC(rms) is applied for 60 seconds between the capacitor lead wires and metal balls.</p> 																					
Insulation Resistance (IR)		10,000 MΩ minimum	The insulation resistance shall be measured with 500±50 VDC applied after 60±5 seconds of charging.																					
Capacitance		Within specified tolerance																						
Dissipation Factor (DF) or Q	<table border="1"> <thead> <tr> <th>Temperature Characteristics</th> <th>Specification</th> </tr> </thead> <tbody> <tr> <td>Y5P, Y5U</td> <td>DF ≤ 2.5%</td> </tr> <tr> <td>Y5V</td> <td>DF ≤ 5.0%</td> </tr> <tr> <td>SL</td> <td>≥ 30 pF: Q ≥ 1000 &lt; 30 pF: Q ≥ 400 +(20 x C) C = Nominal capacitance</td> </tr> </tbody> </table>		Temperature Characteristics	Specification	Y5P, Y5U	DF ≤ 2.5%	Y5V	DF ≤ 5.0%	SL	≥ 30 pF: Q ≥ 1000 < 30 pF: Q ≥ 400 +(20 x C) C = Nominal capacitance	<p>Y5P, Y5U and Y5V: Capacitance is measured at 1 kHz ±20% and 5 Vrms or less. (20±2°C) SL: Capacitance is measured at 1 MHz ±20% and 1.0±0.2 Vrms (25°C)</p>													
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Terminal Strength	Tensile	Lead wire or capacitor body shall not break.	With the termination in its normal position, the specimen is held by its body in such a manner that the axis of the termination is vertical; a tensile force of 10 N is applied to the termination in the direction of its axis and acting in a direction away from the body of the specimen.																					
	Bending	Lead wire or capacitor body shall not break.	With the termination in its normal position, the specimen is held by its body in such a manner that the axis of the termination is vertical; a mass force of 5 N is then suspended from the end of the termination. The body of the specimen is then inclined within a period of 2 to 3 seconds, through an angle of approximately 90° in the vertical plane and then resumed to its initial position over the same period of time; this operation constitutes one bend. One bend immediately followed by a second bend in the opposite direction.																					

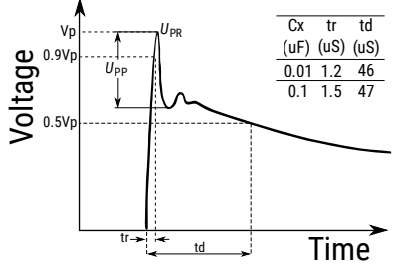
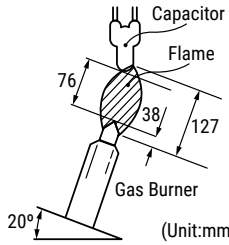
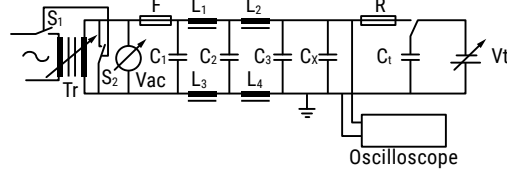
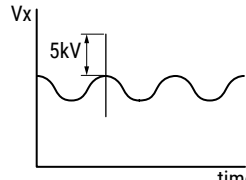
<sup>1</sup> "Room Condition" is defined as follows: Temperature: 15 ~ 35°C/Humidity: 45 ~ 75%/Atmospheric Pressure: 86 ~ 106 kPa.

**Table 2 – Performance & Reliability: Test Methods and Conditions cont'd**

Item		Specification	Test Method												
Solderability		Lead wire should have a uniform coating of solder in the axial direction and over 3/4 of its circumference.	The lead wire of the capacitor is dipped into molten solder for 5±0.5 seconds. The depth of immersion is up to 1.5 mm (+5/-0 mm) from the root of lead wires. Solder Temperature: Lead free solder (Sn-3Ag – 0.5Cu) 245°C ±5°C.												
Soldering Effect (Non-Preheat)	Appearance	No visual defect	<p>As shown in the figure below, the lead wires are immersed in molten solder up to 1.5 mm (+5/-0 mm) from the end of the epoxy meniscus (root of lead wire). Duration/Solder Temperature: 3.5±0.5 seconds/350°C ±10°C or 10±1 seconds/260°C ±5°C</p>  <p><b>Pre-treatment:</b> Capacitor is stored at 85°C ±2°C for 1 hour and then placed at room condition<sup>1</sup> for 24 ±2 hours before initial measurements. <b>Post-treatment:</b> Capacitor is stored for 1 to 2 hours at room condition<sup>1</sup>.</p>												
	IR	1,000 MΩ													
	Dielectric Strength	Per item 1													
	Capacitance	Y5P, Y5U and Y5V: Within ±10% SL: Within ±2.5% or ±0.25 pF, whichever is larger.													
Soldering Effect (Preheat)	Appearance	No visual defect	<p>Capacitor is stored at 120°C +0/-5°C for 60 +0/-5 seconds. Then, as shown in the figure below, the lead wires are immersed in molten solder up to 1.5 mm (+5/-0mm) from the end of the epoxy meniscus (root of lead wire). Duration/Solder Temperature: 7.5 +0/-1 seconds/260°C ±5°C</p>  <p><b>Pre-treatment:</b> Capacitor is stored at 85°C ±2°C for 1 hour and then placed at room condition<sup>1</sup> for 24 ±2 hours before initial measurements. <b>Post-treatment:</b> Capacitor is stored for 1 to 2 hours at room condition<sup>1</sup>.</p>												
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Biased Humidity	Appearance	No visual defect	Steady State Humidity:	Load Humidity:											
	Capacitance	<table border="1"> <thead> <tr> <th>Temperature Characteristics</th> <th>Capacitance Change</th> </tr> </thead> <tbody> <tr> <td>Y5P</td> <td>Within ±10%</td> </tr> <tr> <td>Y5U</td> <td>Within ±20%</td> </tr> <tr> <td>Y5V</td> <td>Within ±30%</td> </tr> <tr> <td>SL</td> <td>Within ±2.5% or ±0.25 pF, whichever is larger.</td> </tr> </tbody> </table>		Temperature Characteristics	Capacitance Change	Y5P	Within ±10%	Y5U	Within ±20%	Y5V	Within ±30%	SL	Within ±2.5% or ±0.25 pF, whichever is larger.	<p>90 to 95% humidity at 40°C ±2°C for 500 ±12 hours.</p> <p><b>Post Treatment:</b> Capacitor is stored for 1 to 2 hours at room condition<sup>1</sup>.</p>	<p>90 to 95% humidity at 40°C ±2°C for 500 ±12 hours with full rated voltage applied.</p> <p><b>Post Treatment:</b> Capacitor is stored for 1 to 2 hours at room condition<sup>1</sup>.</p>
		Temperature Characteristics	Capacitance Change												
		Y5P	Within ±10%												
		Y5U	Within ±20%												
	Y5V	Within ±30%													
	SL	Within ±2.5% or ±0.25 pF, whichever is larger.													
	DF	Y5P and Y5U: 5.0% maximum Y5V: 7.5% maximum SL: Less than 30 pF:													
Q	Q ≥ 100 + 10 × C/3 More than 30 pF: Q ≥ 200 C = Nominal capacitance														
IR	Y5P, Y5V and Y5U: 3,000 MΩ minimum SL: 1,000 MΩ minimum														
Dielectric Strength	No failures														

<sup>1</sup> "Room Condition" is defined as follows: Temperature: 15 ~ 35°C/Humidity: 45 ~ 75%/Atmospheric Pressure: 86 ~ 106 kPa.

**Table 2 – Performance & Reliability: Test Methods and Conditions cont'd**

Item		Specification	Test Method																			
High Temperature Life	Appearance	No visual defect	Impulse Voltage: Each individual capacitor is subjected to three 5 kv impulses prior to life testing.  <table border="1" data-bbox="1136 399 1250 493"> <tr> <td>Cx</td> <td>tr</td> <td>td</td> </tr> <tr> <td>(uF)</td> <td>(uS)</td> <td>(uS)</td> </tr> <tr> <td>0.01</td> <td>1.2</td> <td>46</td> </tr> <tr> <td>0.1</td> <td>1.5</td> <td>47</td> </tr> </table>	Cx	tr	td	(uF)	(uS)	(uS)	0.01	1.2	46	0.1	1.5	47							
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Capacitance Change	Y5P, Y5V and Y5U: Within $\pm 20\%$ SL: Within $\pm 3$ or $\pm 0.3$ pF, whichever is larger.																					
IR	3,000 M $\Omega$ minimum SL: 1,000 M $\Omega$ minimum																					
Dielectric Strength	No failures																					
Flame Test	The capacitor flame extinguishes as follows:	<table border="1" data-bbox="454 882 803 1081"> <thead> <tr> <th>Cycle</th> <th>Time</th> </tr> </thead> <tbody> <tr> <td>1 ~ 4</td> <td>30 seconds maximum</td> </tr> <tr> <td>5</td> <td>60 seconds maximum</td> </tr> </tbody> </table>	Cycle	Time	1 ~ 4	30 seconds maximum	5	60 seconds maximum	The capacitor is exposed to a flame for 15 seconds and then removed for 15 seconds. This test is repeated for 5 cycles. 													
	Cycle		Time																			
1 ~ 4	30 seconds maximum																					
5	60 seconds maximum																					
Active Flammability	The cheesecloth should not ignite.	The capacitors are individually wrapped in at least one, but not more than two, complete layers of cheesecloth. They are then subjected to 20 discharges. The interval between successive discharges is 5 seconds. The VAC is maintained for 2 minutes after the last discharge.  <table border="1" data-bbox="852 1428 1518 1627"> <tbody> <tr> <td>C<sub>1,2</sub></td> <td>1 <math>\mu</math>F <math>\pm 10\%</math></td> <td>C<sub>3</sub></td> <td>0.033 <math>\mu</math>F <math>\pm 5\%</math> 10 kV</td> </tr> <tr> <td>L<sub>1-4</sub></td> <td>1.5 Mh <math>\pm 20\%</math> 16A Rod core choke</td> <td>C<sub>x</sub></td> <td>Test capacitor</td> </tr> <tr> <td>R</td> <td>100<math>\pm 2\%</math></td> <td>V<sub>AC</sub></td> <td>VR <math>\pm 5\%</math></td> </tr> <tr> <td>C<sub>t</sub></td> <td>3 <math>\mu</math>F <math>\pm 5\%</math> 10 kV</td> <td>V<sub>R</sub></td> <td>Rated Voltage</td> </tr> <tr> <td>F</td> <td>Fuse, Rated 10A</td> <td>V<sub>t</sub></td> <td>Voltage applied to Ct</td> </tr> </tbody> </table> 	C <sub>1,2</sub>	1 $\mu$ F $\pm 10\%$	C <sub>3</sub>	0.033 $\mu$ F $\pm 5\%$ 10 kV	L <sub>1-4</sub>	1.5 Mh $\pm 20\%$ 16A Rod core choke	C <sub>x</sub>	Test capacitor	R	100 $\pm 2\%$	V <sub>AC</sub>	VR $\pm 5\%$	C <sub>t</sub>	3 $\mu$ F $\pm 5\%$ 10 kV	V <sub>R</sub>	Rated Voltage	F	Fuse, Rated 10A	V <sub>t</sub>	Voltage applied to Ct
C <sub>1,2</sub>	1 $\mu$ F $\pm 10\%$	C <sub>3</sub>	0.033 $\mu$ F $\pm 5\%$ 10 kV																			
L <sub>1-4</sub>	1.5 Mh $\pm 20\%$ 16A Rod core choke	C <sub>x</sub>	Test capacitor																			
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C <sub>t</sub>	3 $\mu$ F $\pm 5\%$ 10 kV	V <sub>R</sub>	Rated Voltage																			
F	Fuse, Rated 10A	V <sub>t</sub>	Voltage applied to Ct																			

<sup>1</sup> "Room Condition" is defined as follows: Temperature: 15 ~ 35°C/Humidity: 45 ~ 75%/Atmospheric Pressure: 86 ~ 106 kPa.



**Table 2 – Performance & Reliability: Test Methods and Conditions cont'd**

Item		Specification		Test Method																	
Passive Flammability		The burning time should not exceed 30 seconds. The tissue paper should not ignite.		The capacitor under test is held into a flame and in a position which best promotes burning. Each specimen is exposed to the flame one time.																	
				Time of exposure to flame: 30 seconds Length of flame: 12±1 mm Gas burner length: 35 mm minimum Inside diameter: 0.5±0.1 mm Outside diameter: 0.9 mm maximum Gas butane gas purity: 95% minimum																	
Temperature Cycle	Appearance	No visual defect		The capacitor is subjected to 5 temperature cycles. <b>Temperature Cycle</b> <table border="1"> <thead> <tr> <th>Step</th> <th>Temperature (°C)</th> <th>Dwell Time (minutes)</th> <th>Transition Time (minutes)</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>-40+0/-3</td> <td>30</td> <td rowspan="4">3</td> </tr> <tr> <td>2</td> <td>Room temperature</td> <td>3</td> </tr> <tr> <td>3</td> <td>125+3/-0</td> <td>30</td> </tr> <tr> <td>4</td> <td>Room temperature</td> <td>3</td> </tr> </tbody> </table> <p><b>Pre-treatment:</b> Capacitor shall be stored at 85±2 for 1 hour then placed at room condition<sup>1</sup> for 24±2 hours.  <b>Post-treatment:</b> Capacitor is stored for 1 to 2 hours at room condition<sup>1</sup>.</p>	Step	Temperature (°C)	Dwell Time (minutes)	Transition Time (minutes)	1	-40+0/-3	30	3	2	Room temperature	3	3	125+3/-0	30	4	Room temperature	3
	Step	Temperature (°C)	Dwell Time (minutes)		Transition Time (minutes)																
	1	-40+0/-3	30		3																
	2	Room temperature	3																		
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	4	Room temperature	3																		
Capacitance	<b>Temperature Characteristics</b>	<b>Capacitance Change</b>																			
	SL	Within ±5%																			
	Y5P	Within ±10%																			
Y5U, Y5V	Within ±20%																				
DF/Q	SL	≥ 30 pF: Q ≥ 350 < 30 pF: Q ≥ 275 +5/2C C = Nominal capacitance																			
	Y5P	DF ≤ 5%																			
	Y5U, Y5V	DF ≤ 7.5%																			
IR	3,000 MΩ minimum																				
Dielectric Strength	No failures																				

<sup>1</sup> "Room Condition" is defined as follows: Temperature: 15 ~ 35°C/Humidity: 45 ~ 75%/Atmospheric Pressure: 86 ~ 106 kPa.

## Soldering and Mounting Information

### Soldering:

When soldering this product to a PCB/PWB, do not exceed the solder heat resistance specification of the capacitor. Subjecting this product to excessive heating could reflow the solder joint between the lead and ceramic element and/or may result in thermal shocks that can crack the ceramic element.

When soldering these capacitors with a soldering iron, it should be performed under the following conditions:

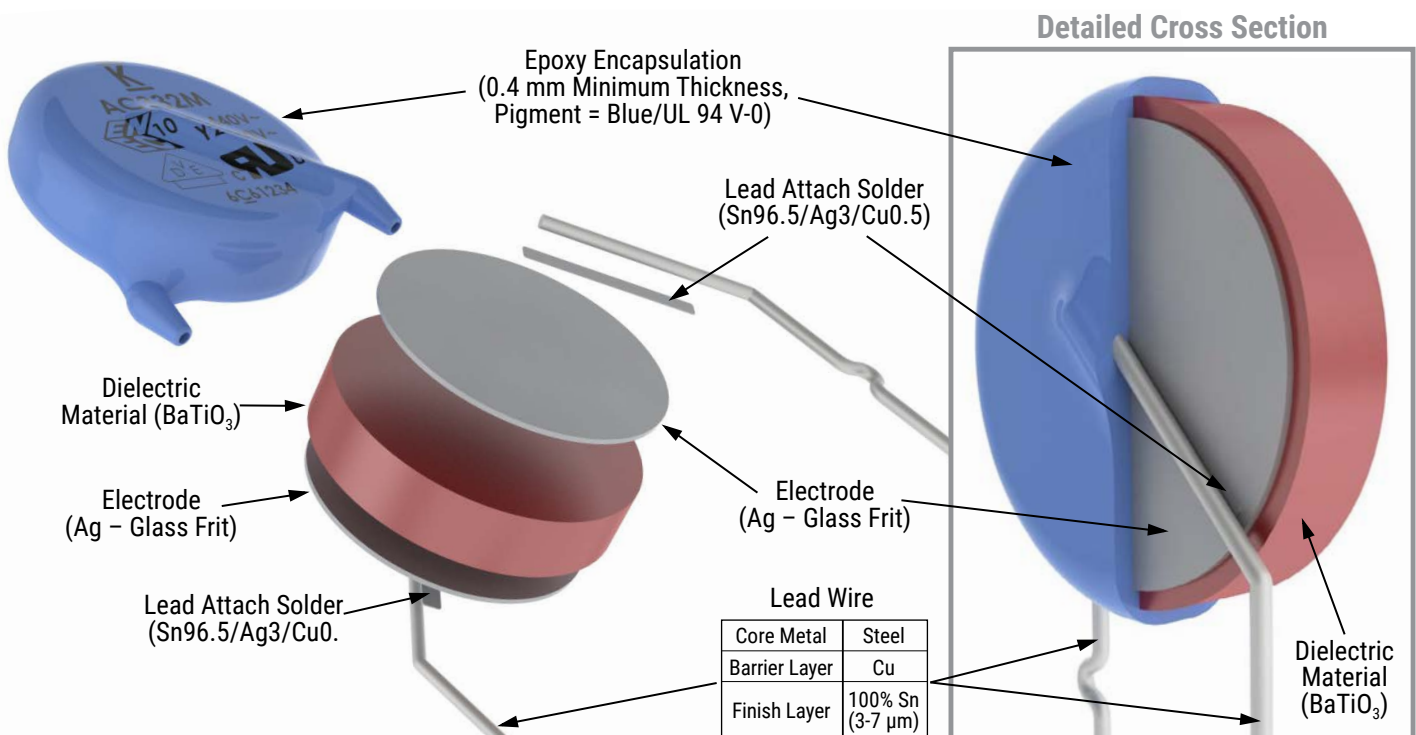
- Temperature of iron-tip: 400°C maximum
- Soldering iron wattage: 50 W maximum
- Soldering time: 3.5 seconds maximum

### Cleaning (ultrasonic cleaning):

To perform ultrasonic cleaning, observe the following conditions:

- Rinse bath capacity: Output of 20 watts per liter or less
- Rinsing time: 5 minute maximum
- Do not vibrate the PCB/PWB directly
- Excessive ultrasonic cleaning may lead to fatigue destruction of the lead wires

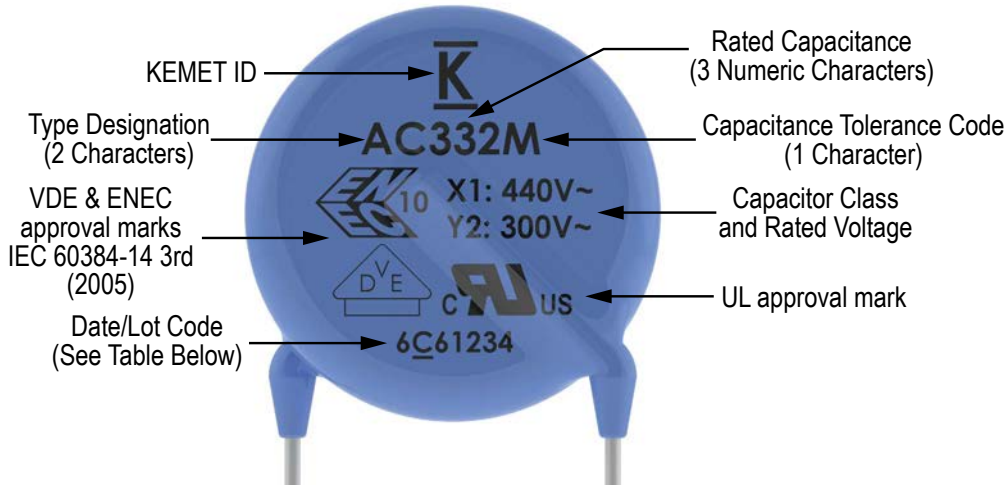
## Construction



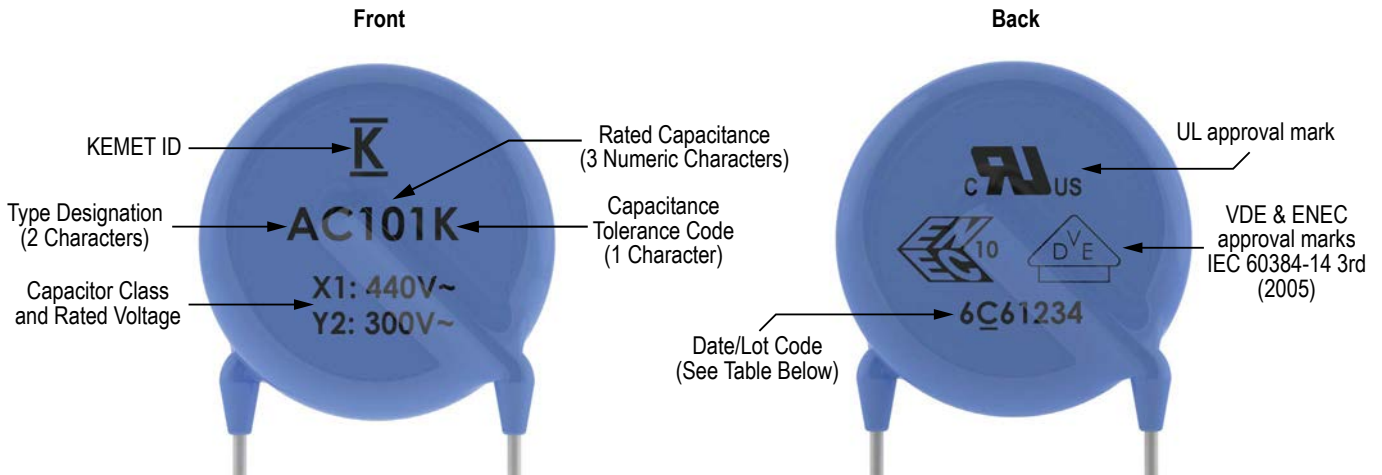
## Marking

These capacitors shall be stamped or laser marked with KEMET's trademark, type designation, capacitor class, rated voltage, rated capacitance and capacitance tolerance codes. In addition, all devices are marked with the recognized approval mark and a date/lot code for traceability. Marking will be supplied either on one side or both sides of the encapsulated capacitor body. All marking shall be legible to allow for clear identification of the component. Marking appears in legible contrast. Illustrated below is an example of the marking format and content. (Two sided marking is limited to capacitors with body diameters ≤ 8.0 mm.)

### Single Sided



### Double Sided



#### Date/Lot Code Explanation

6	C	6	1234
Last digit of year, e.g., 6 = 2016	Manufacturing Location Code	Manufacturing Month: 1-9 = Jan - Sept A = October N = November D = December	Last 4 digits of lot no.

## Packaging Quantities

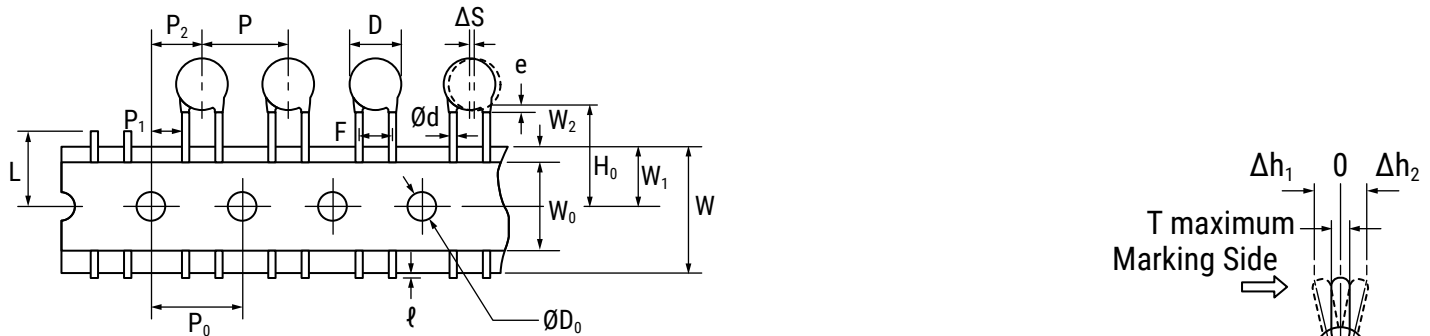
Capacitor Body Diameter (mm)	Body Diameter Code <sup>1</sup>	Bulk Bag (Loose)	Ammo Pack (Carrier Tape)		
			Component pitch on carrier tape <sup>2</sup>		
			12.7 mm	15 mm	25.4 mm
7.0	0	500 pieces/bag	1,000 pieces/box	1,000 pieces/box	1,000 pieces/box
8.0	1				
9.0	2				
10.0	3				
11.0	4				
12.0	5				
13.0	6				500 pieces/box
14.0	7				
15.0	8				

<sup>1</sup> The "Body Diameter Code" is located in the third character position of the ordering code. This code identifies the maximum diameter of the capacitor body in millimeters. For more information regarding the ordering code, see "Ordering Information" section of this document.

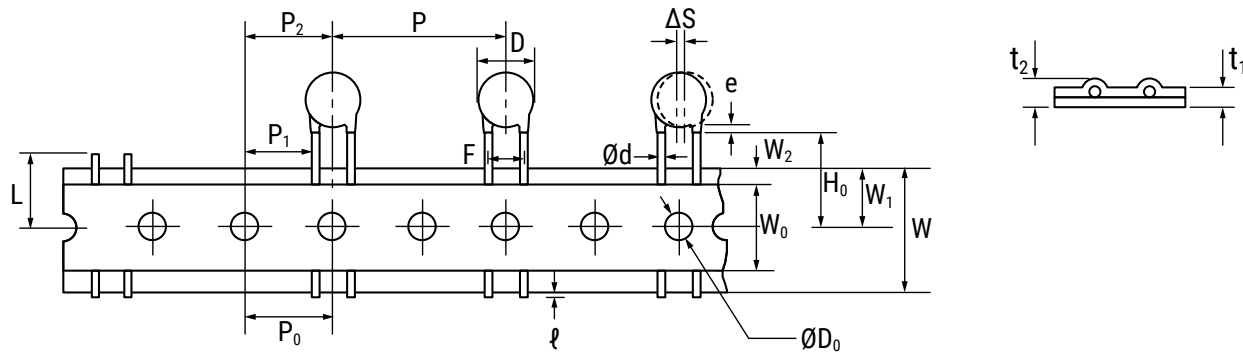
<sup>2</sup> For details regarding component pitch on carrier tape, see "Ammo Pack Taping Format" and "Ammo Pack Taping Specifications" sections of this document.

## Figure 1 - Ammo Pack Taping Format

### 5 mm and 7.5 mm Lead Spacing:



### 10 mm Lead Spacing:



**Table 3 – Ammo Pack Taping Specifications**

Lead Spacing		5 mm		7.5 mm		10 mm	
Lead Style		Straight	Prefromed <sup>1</sup>	Straight	Prefromed <sup>1</sup>	Straight	Prefromed <sup>1</sup>
Item	Symbol	Dimensions (mm)					
Lead Spacing	F	5.0+0.8/-0.2		7.5±1.0		10.0±1.0	
Component Pitch	P	12.7		15.0		25.4±2	
Sprocket Hole Pitch	P <sub>0</sub>	12.7±0.3		15.0±0.3		12.7±0.3	
Sprocket Hole Center to Component Center	P <sub>2</sub>	6.35±1.5		7.5±1.5		12.7±1.5	
Sprocket Hole Center to Lead Center	P <sub>1</sub>	3.75±1.0		3.75±1.0		7.7±1.5	
Body Diameter	D	See "Product Ordering Codes and Ratings" section of this document.					
Component Alignment (side/side)	ΔS	0±2.0					
Carrier Tape Width	W	18.0+1.0/-0.5					

<sup>1</sup> Prefromed (crimped) lead configurations include vertical kink, outside kink and inside kink. See "Lead Configurations" and "Ordering Information" sections of this document for further details.

<sup>2</sup> Also referred to as "lead length" in this document.

**Table 3 – Ammo Pack Taping Specifications cont'd**

Lead Spacing		5 mm		7.5 mm		10 mm	
Lead Style		Straight	Prefromed <sup>1</sup>	Straight	Prefromed <sup>1</sup>	Straight	Prefromed <sup>1</sup>
Item	Symbol	Dimensions (mm)					
Sprocket Hole Position	$W_1$	9.0±0.5					
Height to Seating Plane <sup>2</sup> (prefromed leads <sup>1</sup> )	$H_0$	N/A	18.0+2.0/-0	N/A	18.0+2.0/-0	N/A	18.0+2.0/-0
Height to Seating Plane <sup>2</sup> (straight leads)	H	20.0+1.5/-1.0	N/A	20.0+1.5/-1.0	N/A	20.0+1.5/-1.0	N/A
Lead Protrusion	$\ell$	2.0 maximum					
Diameter of Sprocket Hole	$D_0$	4.0±0.2					
Lead Diameter	$\phi d$	0.55±0.1					
Carrier Tape Thickness	$t_1$	0.6±0.3					
Total Thickness (Carrier Tape, Hold-Down Tape and Lead)	$t_2$	1.5 maximum					
Component Alignment (front/ back)	$\Delta h_1$	2.0 maximum					
	$\Delta h_2$						
Cut Out Length	L	11.0 maximum					
Hold-Down Tape Width	$W_0$	11.0 minimum			11.5 minimum		
Hold-Down Tape Position	$W_2$	3.0 maximum			1.5±1.5		

<sup>1</sup> Prefromed (crimped) lead configurations include vertical kink, outside kink and inside kink. See "Lead Configurations" and "Ordering Information" sections of this document for further details.

<sup>2</sup> Also referred to as "lead length" in this document.

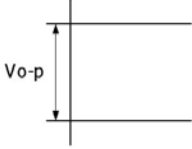
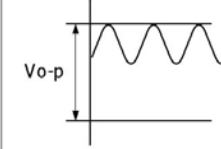
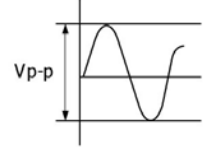
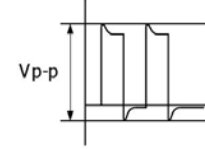
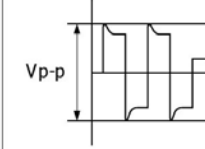
## Application Notes:

### Storage and Operating Conditions:

The insulating coating of these devices does not form an air and moisture-tight seal. Avoid exposure to moisture and do not use or store these devices in a corrosive atmosphere, especially where chloride gas, sulfide gas, acid, alkali, salt, or the like are present. Before cleaning, bonding or molding these devices, it is important to verify that your process does not affect product quality and performance. KEMET recommends testing and evaluating the performance of a cleaned, bonded or molded product prior to implementing and/or qualifying any of these processes. Store the capacitors where the temperature and relative humidity do not exceed 40 degrees Centigrade and 70% respectively. For optimum solderability, capacitor stock should be used promptly, preferably within 6 months of receipt.

### Working Voltage:

Application voltage (Vp-p or Vo-p) must not exceed the voltage rating of the capacitor. Irregular voltages can be generated for a transient period of time when voltage is initially applied and/or removed from a circuit. It is important to choose a capacitor with a voltage rating greater than or equal to these irregular voltages.

Voltage	DC Voltage	DC+AC Voltage	AC Voltage	Pulse Voltage (1)	Pulse Voltage (2)
Positional Measurement					

### Operating Temperature and Self-Generating Heat:

The surface temperature of a capacitor should be kept below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself. When the capacitor is used in a high-frequency current, pulse current or similar current, it may self-generate heat due to dielectric loss. Temperature rise due to self-generated heating should not exceed 20°C (while operated at an atmosphere temperature of 25°C).

### Handling - Vibration and Impact:

Do not expose these devices or their leads to excessive shock or vibration during use.

**FAILURE TO FOLLOW THE ABOVE CAUTIONS MAY RESULT, WORST CASE, IN A SHORT CIRCUIT AND CAUSE FUMING OR PARTIAL DISPERSION WHEN THE PRODUCT IS USED.**

## KEMET Electronic Corporation Sales Offices

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Although all product-related warnings, cautions and notes must be observed, the customer should not assume that all safety measures are indicated or that other measures may not be required.



