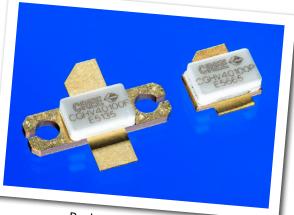


CGHV40100 100 W, DC - 4.0 GHz, 50 V, GaN HEMT

Cree's CGHV40100 is an unmatched, gallium nitride (GaN) high electron mobility transistor (HEMT). The CGHV40100, operating from a 50 volt rail, offers a general purpose, broadband solution to a variety of RF and microwave applications. GaN HEMTs offer high efficiency, high gain and wide bandwidth capabilities making the CGHV40100 ideal for linear and compressed amplifier circuits. The transistor is available in a 2-lead flange and pill package.



Package Types: 440193 & 440206 PN: CGHV40100F & CGHV40100P

Typical Performance Over 500 MHz - 2.5 GHz (T_c = 25°C), 50 V

Parameter	500 MHz	1.0 GHz	1.5 GHz	2.0 GHz	2.5 GHz	Units
Small Signal Gain	17.6	16.9	17.7	17.5	14.8	dB
Saturated Output Power	147	100	141	116	112	W
Drain Efficiency @ P _{sat}	68	56	58	54	54	%
Input Return Loss	6	5.1	10.5	5.5	8.8	dB

Note:

Measured CW in the CGHV40100F-TB application circuit.

Features

- Up to 4 GHz Operation
- 100 W Typical Output Power
- 17.5 dB Small Signal Gain at 2.0 GHz
- Application Circuit for 0.5 2.5 GHz
- 55 % Efficiency at P_{SAT}
- 50 V Operation



Large Signal Models Available for SiC & GaN



Absolute Maximum Ratings (not simultaneous) at 25°C Case Temperature

Parameter	Symbol	Rating	Units	Conditions
Drain-Source Voltage	V _{DSS}	125	Volts	25°C
Gate-to-Source Voltage	V _{GS}	-10, +2	Volts	25°C
Storage Temperature	T _{stg}	-65, +150	°C	
Operating Junction Temperature	T,	225	°C	
Maximum Forward Gate Current	I_{GMAX}	20.8	mA	25°C
Maximum Drain Current ¹	I _{dmax}	8.7	А	25°C
Soldering Temperature ²	Τ _s	245	°C	
Screw Torque	τ	40	in-oz	
Thermal Resistance, Junction to Case ³	R _{ejc}	1.62	°C/W	85°C
Thermal Resistance, Junction to Case ⁴	R _{ejc}	1.72	°C/W	85°C
Case Operating Temperature ⁵	T _c	-40, +150	°C	30 seconds

Note:

¹ Current limit for long term, reliable operation

² Refer to the Application Note on soldering at <u>www.cree.com/products/wireless_appnotes.asp</u>

 3 Measured for the CGHV40100P at $\rm P_{_{DISS}}$ = 83 W. 4 Measured for the CGHV40100F at $\rm P_{_{DISS}}$ = 83 W.

⁵ See also, Power Derating Curve on Page 7

Electrical Characteristics ($T_c = 25^{\circ}C$)

Characteristics	Symbol	Min.	Тур.	Max.	Units	Conditions	
DC Characteristics ¹							
Gate Threshold Voltage	$V_{\rm GS(th)}$	-3.8	-3.0	-2.3	V _{DC}	$V_{_{\rm DS}}$ = 10 V, $I_{_{\rm D}}$ = 20.8 mA	
Gate Quiescent Voltage	$V_{GS(Q)}$	-	-2.7	-	V _{DC}	$V_{_{DS}}$ = 50 V, $I_{_{D}}$ = 0.6 A	
Saturated Drain Current ²	I _{DS}	15.6	18.7	-	А	$V_{_{DS}}$ = 6.0 V, $V_{_{GS}}$ = 2.0 V	
Drain-Source Breakdown Voltage	V _{BR}	150	-	-	V _{DC}	$V_{\rm gS}$ = -8 V, $I_{\rm d}$ = 20.8 mA	
RF Characteristics ³ ($T_c = 25^{\circ}C$, $F_0 = 2.0$ GHz unless otherwise noted)							
Small Signal Gain	G _{ss}	-	17.5	-	dB	$V_{_{\rm DD}}$ = 50 V, $I_{_{\rm DQ}}$ = 0.6 A	
Power Gain	G _p	-	11.0	-	dB	$V_{_{DD}}$ = 50 V, $I_{_{DQ}}$ = 0.6 A, $P_{_{OUT}}$ = $P_{_{SAT}}$	
Power Output at Saturation ⁴	P _{SAT}	-	116	-	W	$V_{_{\rm DD}}$ = 50 V, $I_{_{\rm DQ}}$ = 0.6 A	
Drain Efficiency	η	-	54	-	%	$V_{_{DD}}$ = 50 V, $I_{_{DQ}}$ = 0.6 A, $P_{_{OUT}}$ = $P_{_{SAT}}$	
Output Mismatch Stress	VSWR	-	-	10:1	Ψ	No damage at all phase angles, $V_{_{DD}}$ = 50 V, $I_{_{DQ}}$ = 0.6 A, $P_{_{OUT}}$ = 100 W CW	
Dynamic Characteristics ⁵							
Input Capacitance	C _{gs}	-	29.3	-	pF	$V_{_{\rm DS}}$ = 50 V, $V_{_{\rm gs}}$ = -8 V, f = 1 MHz	
Output Capacitance	C _{DS}	-	7.3	-	pF	$V_{_{\rm DS}}$ = 50 V, $V_{_{\rm gs}}$ = -8 V, f = 1 MHz	
Feedback Capacitance	C _{GD}	-	0.61	-	pF	$V_{_{\rm DS}}$ = 50 V, $V_{_{\rm gs}}$ = -8 V, f = 1 MHz	

Notes:

¹ Measured on wafer prior to packaging.

² Scaled from PCM data.

³ Measured in CGHV40100-TB.

 $^{\rm 4}~{\rm P}_{_{\rm SAT}}$ is defined as $I_{_{\rm G}}$

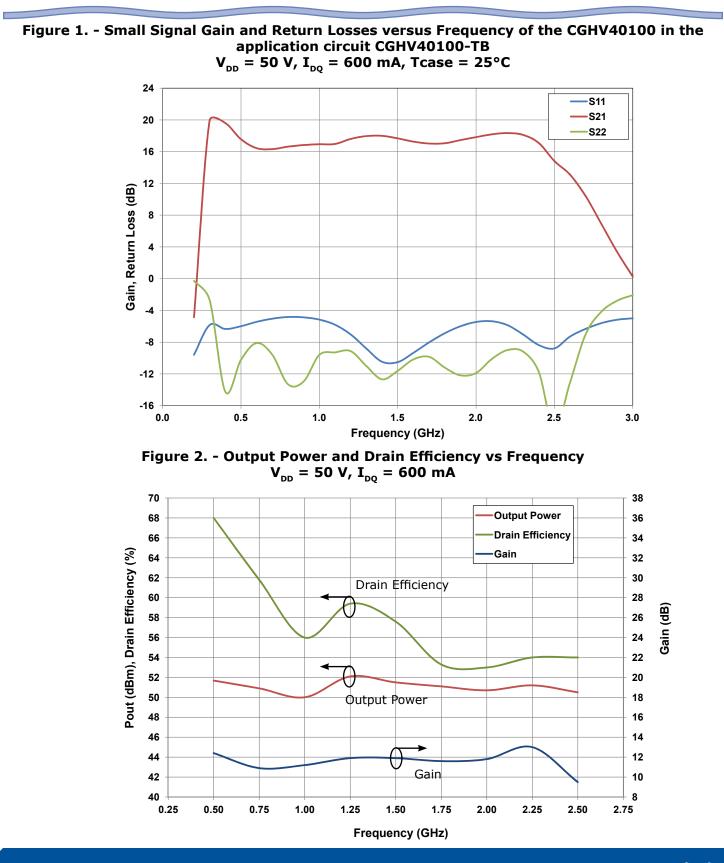
⁵ Includes package

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CGHV40100 Typical Performance



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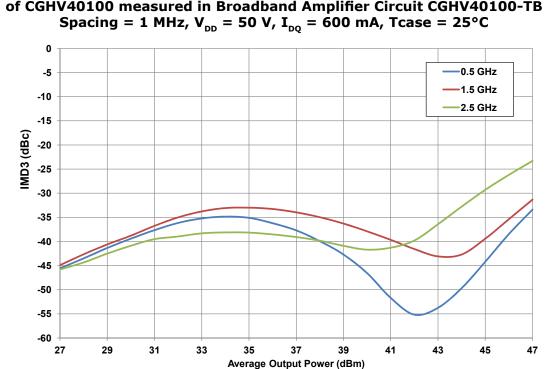
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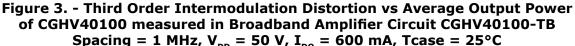
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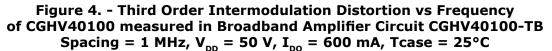
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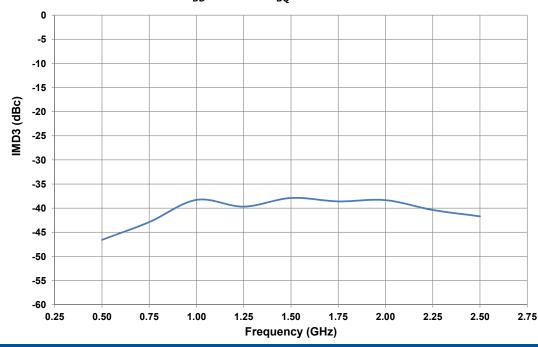


CGHV40100 Typical Performance









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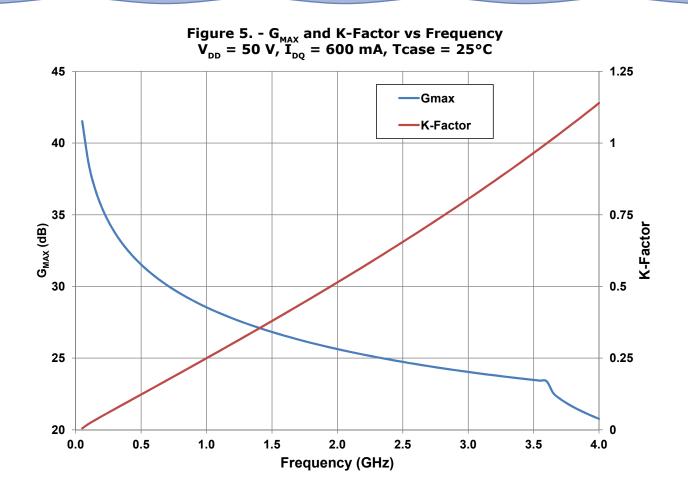
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4



CGHV40100 Typical Performance



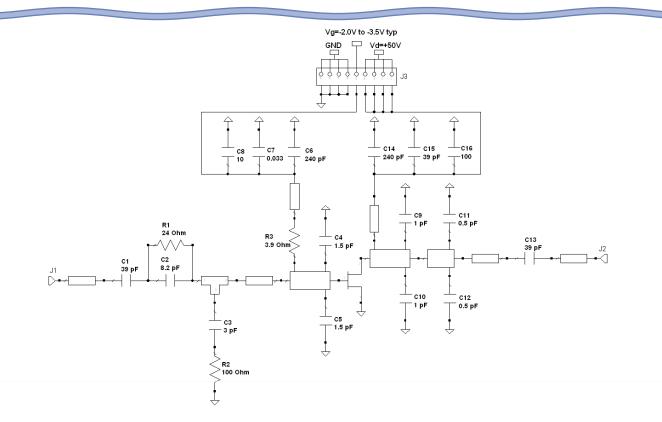
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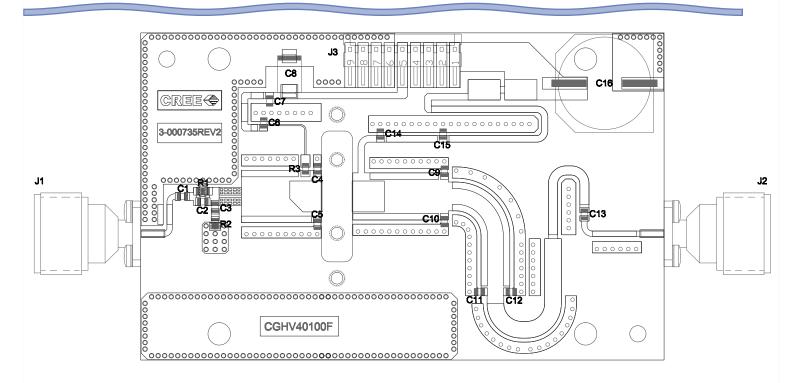
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CGHV40100-TB Application Circuit Schematic



CGHV40100-TB Application Circuit



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CGHV40100-TB Application Circuit Bill of Materials

Designator	Description	Qty
C1, C13, C15	CAP, 39 pF, ± 0.1 pF, 250V, 0805, ATC600F	3
C2	CAP, 8.2 pF, ± 0.1 pF, 250 V, 0806, ATC600F	1
C3	CAP, 3 pF ± 0.1 pF, 250 V, 0805, ATC600F	1
C4, C5	CAP, 1.5 pF, ± 0.1 pF, 250 V, 0805, ATC600F	2
C7	CAP, 33000 pF, 0805 100V, X7R	1
C6, C14	CAP, 240 pF, ± 0.5 pF, 250 V, 0805, ATC600F	2
C8	CAP, 10 UF, 16V TANTALUM, 2312	1
C9, C10	CAP, 1 pF, ± 0.1 pF, 250 V, 0805, ATC600F	2
C11, C12	CAP, 0.5 pF, ± 0.1 pF, 250 V, 0805, ATC600F	2
C16	CAP, 100 UF, 20%, 160 V, ELEC	1
R1	RES, 24 OHMS, IMS ND3-1005CS24R0G	1
R2	RED, 100 OHMS, IMS ND3-0805EW1000G	1
R3	RES, 3.9 OHMS, 0805	1
J1, J2	CONN, N, FEM, W/.500 SMA FLNG	2
J3	HEADER RT>PLZ .1CEN LK 9POS	1
	BASEPLATE, CGH35120	1
	PCB, RO4350B, 2.5" X 4" X 0.020", CGHV40100F	1

CGHV40100-TB Demonstration Amplifier Circuit



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CGHV40100 Power Dissipation De-rating Curve

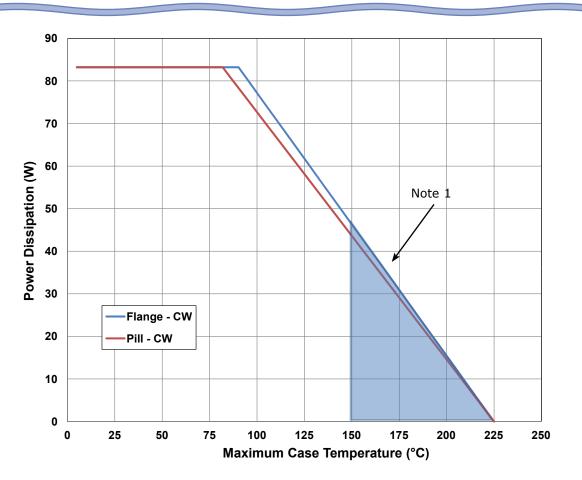


Figure 5. - Transient Power Dissipation De-Rating Curve

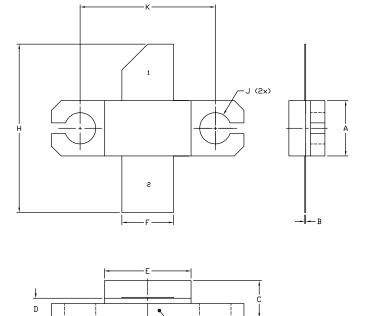
Note 1. Area exceeds Maximum Case Temperature (See Page 2).

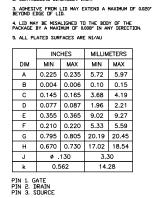
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Product Dimensions CGHV40100F (Package Type - 440193)

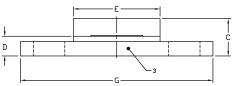




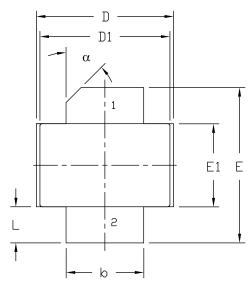
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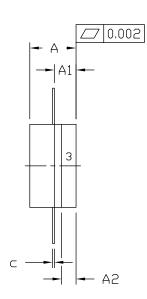
2. CONTROLLING DIMENSION: INCH.

NOTES



Product Dimensions CGHV40100P (Package Type - 440206)





NDTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M -

1994.

2. CONTROLLING DIMENSION: INCH.

3. ADHESIVE FROM LID MAY EXTEND A MAXIMUM OF 0.020" BEYOND EDGE OF LID.

4. LID MAY BE MISALIGNED TO THE BODY OF PACKAGE BY A MAXIMUM OF 0.008" IN ANY DIRECTION.

	INCHES		MILLIM	NOTES	
DIM	MIN	MAX	MIN	MAX	
A	0.125	0.145	3.18	3.68	
A1	0.057	0.067	1.45	1.70	
A2	0.035	0.045	0.89	1.14	
b	0.210	0.220	5.33	5.59	2x
с	0.004	0.006	0.10	0.15	2x
D	0.375	0.385	9.53	9.78	
D1	0.355	0.365	9.02	9.27	
E	0.400	0.460	10.16	11.68	
E1	0.225	0.235	5.72	5.97	
L	0.085	0.115	2.16	2.92	2x
α	45' REF		45*		

PIN 1. GATE

2. DRAIN

3. SOURCE

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