

# 8-Channel ESD Protection Array in Chip Scale Package

CM1205

#### **Features**

- Functionally and pin compatible with CMD's PACDN1408 ESD protection device
- Eight transient voltage suppressors in a single package
- Optiguard<sup>™</sup> coated for improved reliability at assembly
- In-system Electrostatic Discharge (ESD)
   protection to ±25kV contact discharge per IEC
   61000-4-2 international standard
- Compact Chip Scale Package (0.65mm pitch) format saves board space and eases layout in space critical applications compared to discrete solutions and traditional wire bonded packages
- RoHS compliant (lead-free) 10-bump CSP

#### **Applications**

- ESD protection for sensitive electronic equipment
- I/O port, keypad and button circuitry protection for portable devices
- Wireless Handsets
- Handheld PCs / PDAs
- MP3 Players
- Digital Cameras and Camcorders
- Notebooks
- Desktop PCs

#### **Product Description**

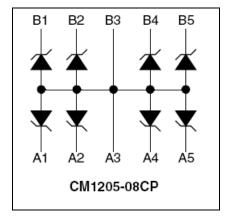
The CM1205 transient voltage suppressor array provides a very high level of protection for sensitive electronic components that may be subjected to ESD.

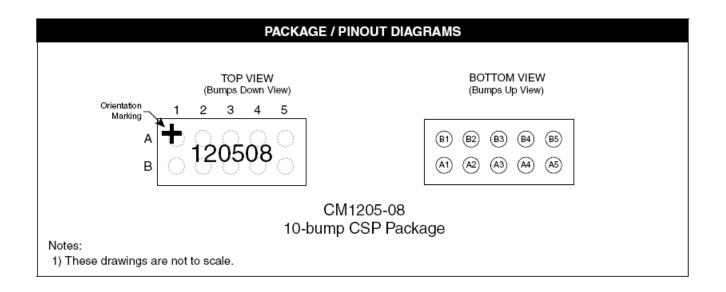
The CM1205 will safely dissipate ESD strikes at levels well beyond the maximum requirements set forth in the IEC 61000-4-2 international standard (Level 4, ±8kV contact discharge). All I/Os are rated at ±25kV using the IEC 61000-4-2 contact discharge method. Using the MIL-STD-883D (Method 3015) specification for Human Body Model (HBM) ESD, all pins are protected for contact discharges to greater than +30kV.

The Chip Scale Package format of this device enables extremely small footprints that are necessary in portable electronics such as cellular phones, PDAs, internet appliances and PCs. The large solder bumps allow for standard attachment to laminate boards without the use of underfill.

The CM1205 features *OptiGuard*<sup>™</sup> coating for improved reliability at assembly and is available with RoHS compliant lead-free finishing.

#### **Block Diagram**





# **Ordering Information**

PART NUMBERING INFORMATION							
Bumps	Package	Ordering Part Number <sup>1</sup>	Part Marking				
10	CSP	CM1205-08CP	120508				

Note 1: Parts are shipped in Tape & Reel form unless otherwise specified.

# **Specifications**

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	RATING	UNITS				
Storage Temperature Range	-65 to +150	℃				

STANDARD OPERATING CONDITIONS					
PARAMETER	RATING	UNITS			
Operating Temperature Range	-40 to +85	ç			

ELECTRICAL OPERATING CHARACTERISTICS <sup>1</sup>								
SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS		
V <sub>REV</sub>	Reverse Standoff Voltage	I <sub>DIODE</sub> =10μA		6.0		V		
I <sub>LEAK</sub>	Leakage Current	V <sub>IN</sub> =3.3V DC			100	nA		
V <sub>SIG</sub>	Signal Clamp Voltage Positive Clamp Negative Clamp	I <sub>LOAD</sub> = 10mA	5.6 -1.2	6.8 -0.8	8.0 -0.4	V		
V <sub>ESD</sub>	In-system ESD Withstand Voltage a) Human Body Model, MIL-STD-883, Method 3015 b) Contact Discharge per IEC 61000-4-2 Level 4	Note 2	<u>+</u> 30 <u>+</u> 25			kV kV		
V <sub>CL</sub>	Clamping Voltage during ESD Discharge MIL-STD-883 (Method 3015), 8kV Positive Transients Negative Transients	Note 2		+12 -8		V		
С	Channel Capacitance	At 2.5V DC, f = 1MHz,		39	47	pF		

Note 1:  $T_A=25\,^{\circ}\text{C}$  unless otherwise specified. GND in this document refers to the lower supply voltage. Note 2: ESD applied to channel pins with respect to GND, one at a time. All other channels are open. All GND pins tied to ground.

## **Application Information**

Refer to Application Note AP-217, "The Chip Scale Package", for a detailed description of Chip Scale Packages offered by California Micro Devices.

PRINTED CIRCUIT BOARD RECOMMENDATIONS					
PARAMETER	VALUE				
Pad Size on PCB	0.275mm				
Pad Shape	Round				
Pad Definition	Non-Solder Mask defined pads				
Solder Mask Opening	0.325mm Round				
Solder Stencil Thickness	0.125mm - 0.150mm				
Solder Stencil Aperture Opening (laser cut, 5% tapered walls)	0.330mm Round				
Solder Flux Ratio	50/50 by volume				
Solder Paste Type	No Clean				
Pad Protective Finish	OSP (Entek Cu Plus 106A)				
Tolerance — Edge To Corner Ball	<u>+</u> 50μm				
Solder Ball Side Coplanarity	<u>+</u> 20μm				
Maximum Dwell Time Above Liquidous	60 seconds				
Maximum Soldering Temperature	260ûC				

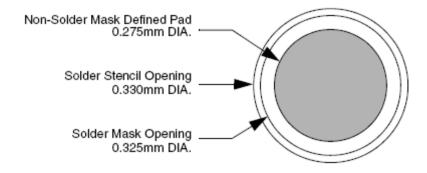


Figure 3. Recommended Non-Solder Mask Defined Pad Illustration

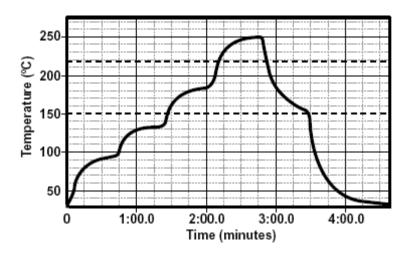
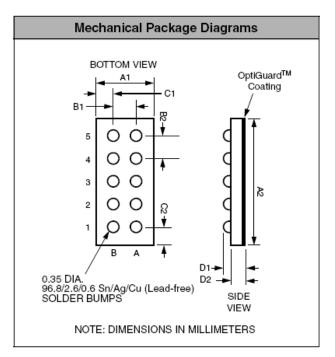


Figure 4. Lead-free (SnAgCu) Solder Ball Reflow Profile

## **Mechanical Details**

The CM1205 is offered in a custom Chip Scale Package (CSP). Dimensions are presented below.

PACKAGE DIMENSions							
Pack	age	Custom CSP					
Bumps		10					
Dim			lillimeters		Inches		
Dilli	Min	Nom	Max	Min	Nom	Max	
<b>A</b> 1	1.109	1.154	1.199	0.0437	0.0454	0.0472	
A2	3.059	3.104	3.149	0.1204	0.1222	0.1240 0.0258 0.0258 0.0119 0.0119	
B1	0.645	0.650	0.655	0.0254	0.0256		
B2	0.645	0.650	0.655	0.0254	0.0256		
C1	0.202	0.252	0.302	0.0080	0.0099		
C2	0.202	0.252	0.302	2 0.0080 0.009	0.0099		
D1	0.612	0.682	0.751	0.0241	0.0268	0.0296	
D2	<b>2</b> 0.368		0.470 0.0145		0.0165	0.0185	
# per ta		3500 pieces					
Controlling dimension: millimeters							



Package Dimensions for CM1205 Chip Scale Package

# **CSP Tape and Reel Specifications**

PART NUMBER	CHIP SIZE (mm)	POCKET SIZE (mm) B <sub>o</sub> X A <sub>o</sub> X K <sub>o</sub>	TAPE WIDTH W	REEL DIAMETER	QTY PER REEL	P <sub>o</sub>	P <sub>1</sub>
CM1205-08CP	3.104 X 1.154 X 0.682	3.28 X 1.32 X 0.81	8mm	178mm (7")	3500	4mm	4mm

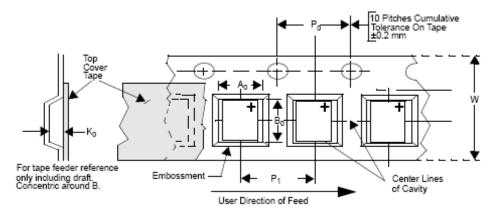


Figure 5. Tape and Reel Mechanical Data

CM1205

ON Semiconductor and are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

#### PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA

Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada

Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855

Toll Free USA/Canada

Europe, Middle East and Africa Technical Support:

Phone: 421 33 790 2910

Japan Customer Focus Center

Phone: 81-3-5773-3850

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative