

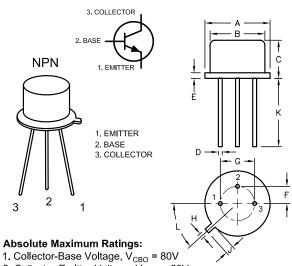
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SPC-F005.DW	í

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	DCP #	REV	DESCRIPTION	DRAWN	DATE	CHECKD	DATE	APPRVD	DATE
	1447	Α	RELEASED	HYO	5/15/02	JWM	2/20/04	JC	2/20/04
	1885	В	UPDATED TO ROHS COMPLIANCE	EO	02/03/06	Ю	2/6/06	НО	2/6/06

Dimensions	Α	В	С	D	Е	F	G	Н	J	K	L
Min.	8.50	7.74	6.09	0.40	-	2.41	4.82	0.71	0.73	12.70	42°
Max.	9.39	8.50	6.60	0.53	0.88	2.66	5.33	0.86	1.02	-	48°





- 2. Collector-Emitter Voltage, $V_{CEO} = 60V$
- 3. Emitter-Base Voltage, V_{EBO} = 5V
- 4. Continuous Collector Current, I_C = 0.7A
- 5. Total Device Dissipation ($T_A = +25$ °C), $P_D = 800$ mW Derate above 25°C = 4.6mW/°C
- 6. Total Device Dissipation ($T_C = +25$ °C), $P_D = 5W$ Derate above 25°C = 28.6mW/°C
- 7. Operating Junction Temperature Range, $T_J = -65^{\circ}$ to +200°C
- 8. Storage Temperature Range, T_{stq} = -65° to +200°C

This is a silicon NPN transistor in a TO-39 type case designed primarily for amplifier and switching applications. This device features high breakdown voltage, low leakage current, low capacity, and beta useful over an extremely wide current range.

Electrical Characteristics: (T_A = +25°C Unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Max	Unit
OFF Characteristics					
Collector-Emitter Breakdown Voltage	V _{(BR)CEO}	$I_{\rm C} = 0.1 {\rm mA}, I_{\rm B} = 0$	60	-	٧
Collector-Base Breakdown Voltage	V _{(BR)CBO}	$I_{\rm C} = 100 \mu {\rm A}, I_{\rm E} = 0$	80	-	٧
Emitter-Base Breakdown Voltage	V _{(BR)EBO}	$I_E = 100 \mu A, I_C = 0$	5	-	٧
Emitter Cut-Off Current	I _{EBO}	$V_{BE} = 4V, I_C = 0$	-	0.25	μΑ

ON Characteristics, Note 1

DC Current Gain	h _{FE}	$V_{CE} = 10V, I_{C} = 150mA$	50	-	250
		$V_{CE} = 2.5V, I_{C} = 150mA$	25	-	-
Collector-Emitter Saturation Voltage	V _{CE(sat)}	I _C = 150mA, I _B = 15mA	-	1.4	٧
Base-Emitter Saturation Voltage	V _{BE(sat)}	I _C = 150mA, I _B = 15mA	-	1.0	V

Small-Signal Characteristics

Current Gain-Bandwidth Product	f _T	$V_{CE} = 10V, I_{C} = 50mA, f = 20MHz$	100	-	MHz
Output Capacitance	C _{obo}	$V_{CB} = 10V, I_{E} = 0, f = 1MHz$	-	12	pF
Input Capacitance	C _{ibo}	$V_{BE} = 500 \text{mV}, I_{C} = 0, f = 1 \text{MHz}$	_	80	pF

Note 1. Pulse Test: Pulse Width ≦300µs, Duty Cycle ≦1%.

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DISCLAIMER:	TOLERANCES:	DRAWN BY:	DATE:	DRAWING TITLE:
ALL STATEMENTS AND TECHNICAL INFORMATION CONTAINED	UNLESS OTHERWISE	HISHAM ODISH	5/15/02	Transistor, Bipolar, Metal, TO-39, NPN
HEREIN ARE BASED UPON INFORMATION AND/OR TESTS WE BELIEVE TO BE ACCURATE AND RELIABLE. SINCE	SPECIFIED,	CHECKED BY:	DATE:	SIZE DWG. NO. ELECTRONIC FILE REV
CONDITIONS OF USE ARE BEYOND OUR CONTROL, THE USER SHALL DETERMINE THE SUITABILITY OF THE PRODUCT	DIMENSIONS ARE	JEFF MCVICKER	2/20/04	A 2N3053A 35C0699.DWG B
FOR THE INTENDED USE AND ASSUME ALL RISK AND LIABILITY WHATSOEVER IN CONNECTION THEREWITH.	PURPOSES ONLY.	APPROVED BY:	DATE:	
		JOHN COLE	2/20/04	SCALE: NTS U.O.M.: Millimeters SHEET: 1 OF 1