2SB1221

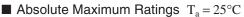
Silicon PNP epitaxial planar type

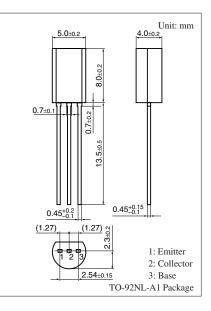
For general amplification

Features

- Low collector-emitter saturation voltage $V_{CE(sat)}$
- Allowing supply with the radial taping

Absolute maximum matings $T_a = 25$ C						
Parameter	Symbol	Rating	Unit			
Collector-base voltage (Emitter open)	V _{CBO}	-250	V			
Collector-emitter voltage (Base open)	V _{CEO}	-200	V			
Emitter-base voltage (Collector open)	V _{EBO}	-5	V			
Collector current	I _C	-70	mA			
Peak collector current	I _{CP}	-100	mA			
Collector power dissipation	P _C	1	W			
Junction temperature	Tj	150	°C			
Storage temperature	T _{stg}	-55 to +150	°C			





Electrical Characteristics $T_a = 25^{\circ}C \pm 3^{\circ}C$

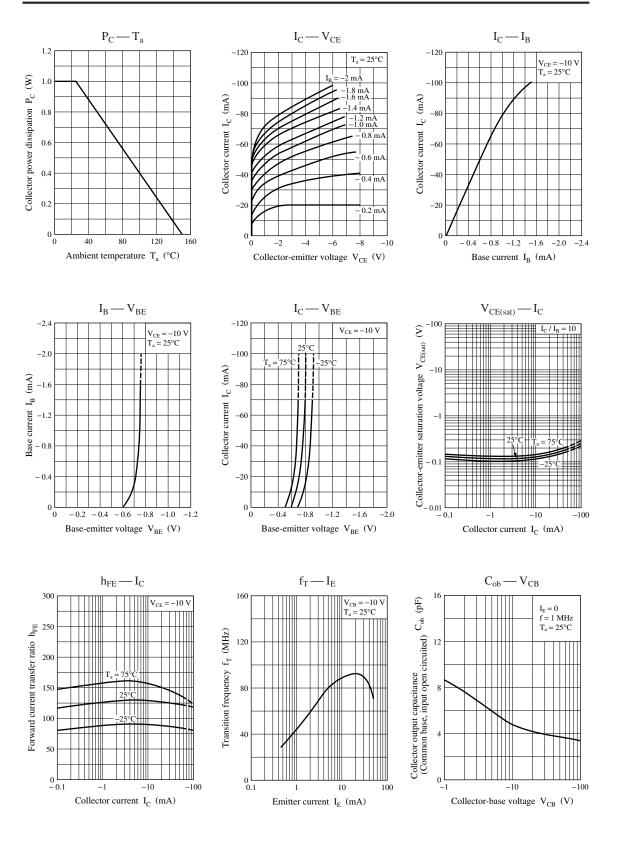
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-emitter voltage (Base open)	V _{CEO}	$I_{\rm C} = -100 \ \mu A, \ I_{\rm B} = 0$	-200			V
Emitter-base voltage (Collector open)	V _{EBO}	$I_E = -1 \ \mu A, \ I_C = 0$	-5			V
Forward current transfer ratio *	h _{FE}	$V_{CE} = -10 \text{ V}, I_C = -5 \text{ mA}$	30		220	
Collector-emitter saturation voltage	V _{CE(sat)}	$I_{\rm C} = -50$ mA, $I_{\rm B} = -5$ mA			-1.5	V
Transition frequency	f _T	$V_{CB} = -10 \text{ V}, I_E = 10 \text{ mA}, f = 200 \text{ MHz}$	50	80		MHz
Collector output capacitance	C _{ob}	$V_{CB} = -10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$		5	10	pF
(Common base, input open circuited)						

Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

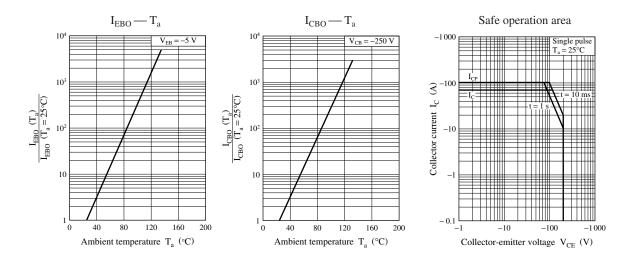
2. *: Rank classification

Rank	Р	Q	R
$h_{\rm FE}$	30 to 100	60 to 150	100 to 220

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