

Vishay Semiconductors

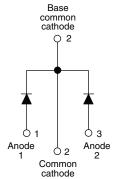
RoHS

HALOGEN

FREE

Schottky Rectifier, 2 x 35 A

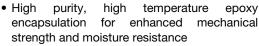


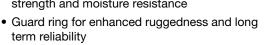


PRODUCT SUMMARY							
Package	TO-247AC						
I _{F(AV)}	2 x 35 A						
V_R	30 V						
V _F at I _F	0.43 V						
I _{RM} max.	450 mA at 125 °C						
T _J max.	150 °C						
Diode variation	Common cathode						
E _{AS}	27 mJ						

FEATURES

- 150 °C T_J operation
- · Low forward voltage drop
- High frequency operation

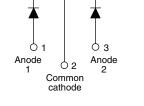




- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)



The VS-72CPQ030... center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.



MAJOR RATINGS AND CHARACTERISTICS								
SYMBOL	CHARACTERISTICS	VALUES	UNITS					
I _{F(AV)}	Rectangular waveform	70	A					
V _{RRM}		30	V					
I _{FSM}	t _p = 5 µs sine	2180	A					
V _F	35 Apk, T _J = 125 °C (per leg)	0.43	V					
TJ	Range	- 55 to 150	°C					

VOLTAGE RATINGS								
PARAMETER SYMBOL VS-72CPQ030PbF VS-72CPQ030-N3 UNITS								
Maximum DC reverse voltage	V _R	30	30	V				
Maximum working peak reverse voltage	V _{RWM}	30	30	V				

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST COND	TEST CONDITIONS					
Maximum average per le	9			35				
forward current See fig. 5 per device	e I _{F(AV)}	50 % duty cycle at T _C = 125 °C, rectangular waveform		70				
Maximum peak one cycle non-repetitive surge current per leg	lea.	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	2180	- A			
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	600				
Non-repetitive avalanche energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 6 A, L = 1.5 mH		27	mJ			
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T_J maximum $V_A = 1.5 \times V_R$ typical		6	А			

Revision: 31-Aug-11 Document Number: 94254



VS-72CPQ030PbF, VS-72CPQ030-N3

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ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS				
Maximum forward voltage drop per leg See fig. 1		35 A	T _{.1} = 25 °C	0.51	V			
	V _{FM} ⁽¹⁾	70 A	- IJ=25 C	0.61				
	VFM (")	35 A	T _ 105 °C	0.43				
		70 A	- T _J = 125 °C	0.58				
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V Dated V	1.9	mA			
See fig. 2		T _J = 125 °C	V _R = Rated V _R	450				
Threshold voltage	V _{F(TO)}	T T		0.25	V			
Forward slope resistance	r _t	ı j = ı j maxımum	$T_J = T_J$ maximum		mΩ			
Maximum junction capacitance per leg	C _T	V _R = 5 V _{DC} (test signal ran	4600	pF				
Typical series inductance per leg	L _S	Measured lead to lead 5 m	7.5	nH				
Maximum voltage rate of change	dV/dt	Rated V _R	10 000	V/µs				

Note

 $^{^{(1)}~}$ Pulse width $<300~\mu s,~duty~cycle < 2~\%$

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and storage temperature range		T _J , T _{Stg}		- 55 to 150	°C			
Maximum thermal resistance, junction to case per leg		В	DC operation See fig. 4	0.8				
Maximum thermal resistance, junction to case per package		R_{thJC}	DC operation	0.4	°C/W			
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.25				
Approximate weight				6	g			
Approximate weight				0.21	OZ.			
Mounting toyour	minimum			6 (5)	kgf · cm			
Mounting torque —	maximum			12 (10)	(lbf \cdot in)			
Marking device			Case style TO-247AC (JEDEC)	72CP	Q030			

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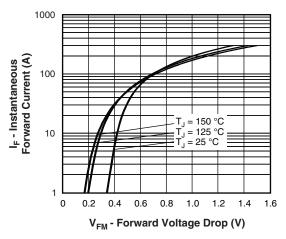


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

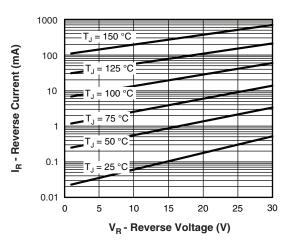


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

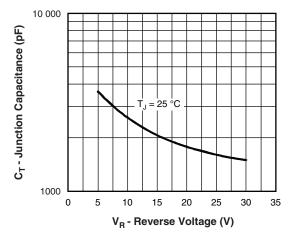


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

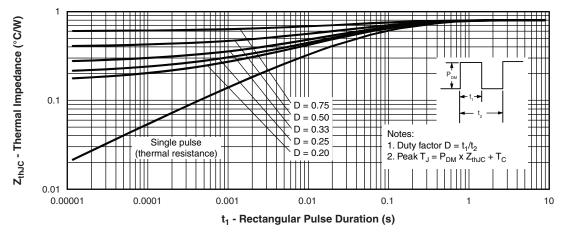


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)

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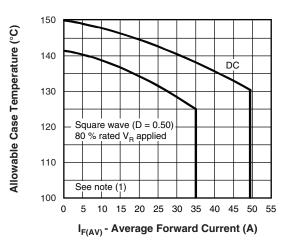


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

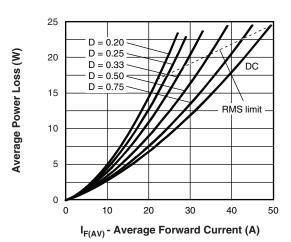


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

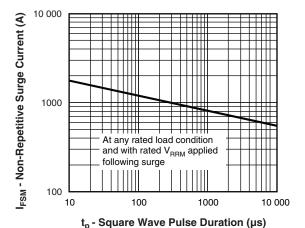


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

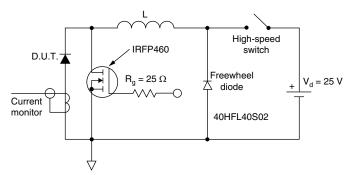


Fig. 8 - Unclamped Inductive Test Circuit

Note

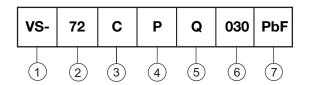
 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{th,JC}; Pd = Forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R

VS-72CPQ030PbF, VS-72CPQ030-N3

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ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating (70 A)

Circuit configuration:

C = Common cathode

4 - Package:

P = TO-247

5 - Schottky "Q" series

Voltage code (030 = 30 V)

7 - Environmental digit

• PbF = Lead (Pb)-free and RoHS compliant

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)									
PREFERRED P/N QUANTITY PER T/R MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION									
VS-72CPQ030PbF	25	500	Antistatic plastic tube						
VS-72CPQ030-N3	25	500	Antistatic plastic tube						

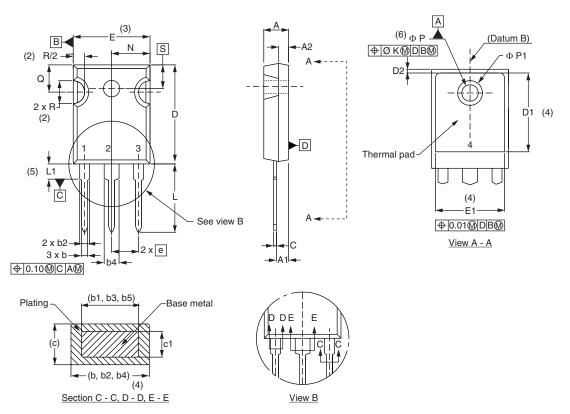
LINKS TO RELATED DOCUMENTS							
Dimensions www.vishay.com/doc?95223							
Part marking information	TO-247AC PbF	www.vishay.com/doc?95226					
	TO-247AC -N3	www.vishay.com/doc?95007					



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TO-247

DIMENSIONS in millimeters and inches



SYMBOL	MILLIM	MILLIMETERS		INCHES		NOTES SY	SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STIVIBOL	MIN.	MAX.	MIN.	MAX.	NOTES	'	STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209			D2	0.51	1.30	0.020	0.051	
A1	2.21	2.59	0.087	0.102			Е	15.29	15.87	0.602	0.625	3
A2	1.50	2.49	0.059	0.098			E1	13.72	-	0.540	-	
b	0.99	1.40	0.039	0.055			е	5.46	BSC	0.215	BSC	
b1	0.99	1.35	0.039	0.053			ØΚ	2.	54	0.0)10	
b2	1.65	2.39	0.065	0.094			L	14.20	16.10	0.559	0.634	
b3	1.65	2.34	0.065	0.092			L1	3.71	4.29	0.146	0.169	
b4	2.59	3.43	0.102	0.135			Ν	7.62	BSC	0	.3	
b5	2.59	3.38	0.102	0.133			ØΡ	3.56	3.66	0.14	0.144	
С	0.38	0.89	0.015	0.035			Ø P1	-	6.98	-	0.275	
c1	0.38	0.84	0.015	0.033			Q	5.31	5.69	0.209	0.224	
D	19.71	20.70	0.776	0.815	3		R	4.52	5.49	0.178	0.216	
D1	13.08	-	0.515	-	4		S	5.51	BSC	0.217	BSC	

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- $^{(7)}\,$ Outline conforms to JEDEC® outline TO-247 with exception of dimension c



Legal Disclaimer Notice

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Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as RoHS-Compliant fulfill the definitions and restrictions defined under Directive 2011/65/EU of The European Parliament and of the Council of June 8, 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment (EEE) - recast, unless otherwise specified as non-compliant.

Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

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