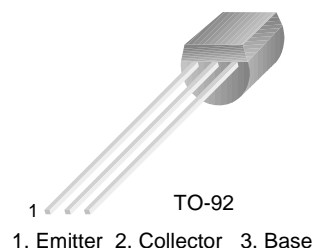


2N7051

2N7051

NPN Darlington Transistor

- This device designed for applications requiring extremely high gain at collector currents to 1.0A and high breakdown voltage.
- Sourced from Process 06.
- See 2N7052 for Characteristics.



NPN Epitaxial Silicon Transistor

Absolute Maximum Ratings* $T_A=25^{\circ}\text{C}$ unless otherwise noted

Symbol	Parameter	Ratings	Units
V_{CEO}	Collector-Emitter Voltage	100	V
V_{CBO}	Collector-Base Voltage	100	V
V_{EBO}	Emitter-Base Voltage	12	V
I_C	Collector Current	1.5	A
T_J, T_{STG}	Storage Temperature	-55 ~ 150	$^{\circ}\text{C}$

* These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

NOTES:

1. These ratings are based on a maximum junction temperature of 150 degrees C.
2. These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

Electrical Characteristics $T_A=25^{\circ}\text{C}$ unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristics						
BV_{CEO}	Collector-Emitter Breakdown Voltage *	$I_C = 1.0\text{mA}, I_B = 0$	100			V
BV_{CBO}	Collector-Base Breakdown Voltage	$I_C = 100\mu\text{A}, I_B = 0$	100			V
BV_{EBO}	Emitter-Base Breakdown Voltage	$I_E = 1.0\text{mA}, I_C = 0$	12			V
I_{CBO}	Collector Cut-off Current	$V_{CB} = 80\text{V}, I_E = 0$			0.1	μA
I_{CES}		$V_{CE} = 80\text{V}, I_E = 0$			0.2	μA
I_{EBO}	Emitter Cut-off Current	$V_{EB} = 7.0\text{V}, I_C = 0$			0.1	μA
On Characteristics *						
h_{FE}	DC Current Gain	$V_{CE} = 5.0\text{V}, I_C = 100\text{mA}$ $V_{CE} = 5.0\text{V}, I_C = 1.0\text{A}$	10,000 1,000		20,000	
$V_{CE}(\text{sat})$	Collector-Emitter Saturation Voltage	$I_C = 100\text{mA}, I_B = 0.1\text{mA}$			1.5	V
$V_{BE}(\text{sat})$	Base-Emitter Saturation Voltage	$I_C = 100\text{mA}, V_{BE} = 5.0\text{V}$			2.0	V
Small Signal Characteristics						
f_T	Transition Frequency	$I_C = 100\text{mA}, V_{CE} = 5.0\text{V}$	200			MHz
h_{fe}	Small Signal Current Gain	$V_{CE} = 5.0\text{V}, I_C = 100\text{mA},$ $f = 20\text{MHz}$	10		100	

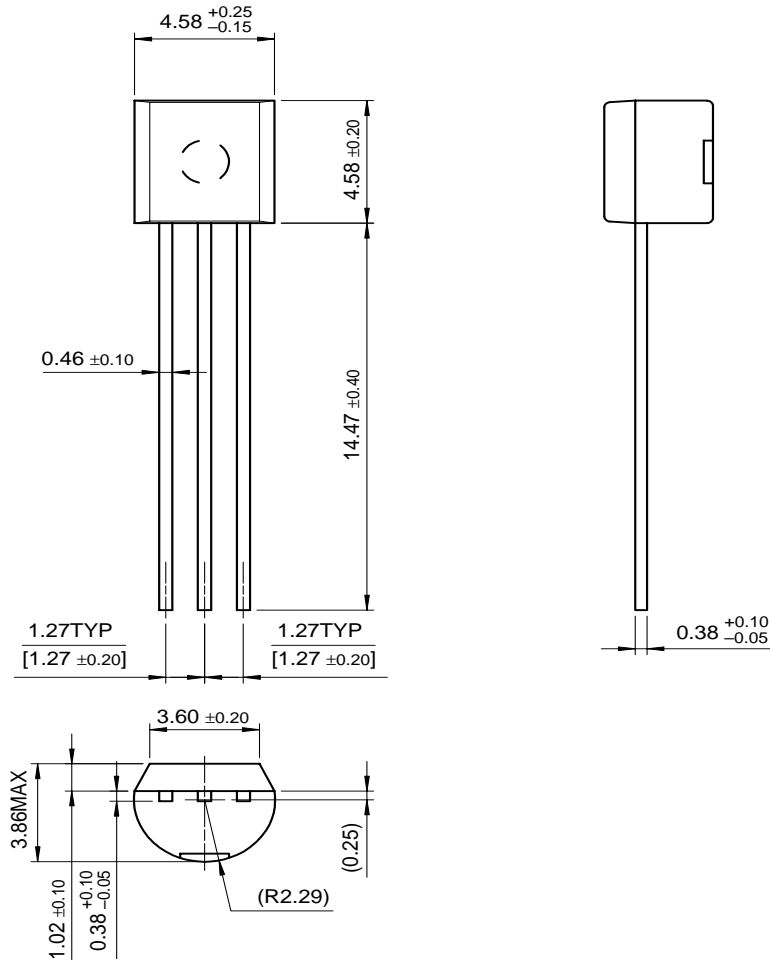
* Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 1.0\%$

Thermal Characteristics $T_A=25^{\circ}\text{C}$ unless otherwise noted

Symbol	Parameter	Max.	Units
P_D	Total Device Dissipation	625	mW
	Derate above 25°C	5.0	mW/ $^{\circ}\text{C}$
$R_{\theta JC}$	Thermal Resistance, Junction to Case	83.3	$^{\circ}\text{C/W}$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	200	$^{\circ}\text{C/W}$

Package Dimensions

TO-92



Dimensions in Millimeters

TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

ACE [™]	FACT [™]	ImpliedDisconnect [™]	PACMAN [™]	SPM [™]
ActiveArray [™]	FACT Quiet series [™]	ISOPLANAR [™]	POP [™]	Stealth [™]
Bottomless [™]	FAST [®]	LittleFET [™]	Power247 [™]	SuperSOT [™] -3
CoolFET [™]	FAST [™]	MicroFET [™]	PowerTrench [®]	SuperSOT [™] -6
CROSSVOL [™]	FRFET [™]	MicroPak [™]	QFET [™]	SuperSOT [™] -8
DOMET [™]	GlobalOptoisolator [™]	MICROWIRE [™]	QS [™]	SyncFET [™]
EcoSPARK [™]	GTO [™]	MSX [™]	QT Optoelectronics [™]	TinyLogic [™]
E ² CMOS [™]	HiSeC [™]	MSXPro [™]	Quiet Series [™]	TruTranslation [™]
EnSigna [™]	I ² C [™]	OCX [™]	RapidConfigure [™]	UHC [™]
Across the board. Around the world. [™]		OCXPro [™]	RapidConnect [™]	UltraFET [®]
The Power Franchise [™]		OPTOLOGIC [®]	SILENT SWITCHER [®]	VCX [™]
Programmable Active Droop [™]		OPTOPLANAR [™]	SMART START [™]	

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.

This datasheet has been download from:

www.datasheetcatalog.com

Datasheets for electronics components.