

Normally – OFF Silicon Carbide Super Junction Transistor

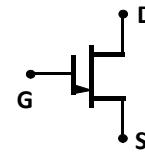
V_{DS}	=	650 V
V_{DS(ON)}	=	1.7 V
I_D	=	4 A
R_{DS(ON)}	=	415 mΩ

Features

- 250 °C maximum operating temperature
- Temperature independent switching performance
- Electrically isolated base-plate
- Gate oxide free SiC switch
- Suitable for connecting an anti-parallel diode
- Positive temperature coefficient for easy paralleling
- Low gate charge
- Low intrinsic capacitance

Package

- RoHS Compliant



TO – 257 (Isolated Base-plate Hermetic Package)

Advantages

- Low switching losses
- Higher efficiency
- High temperature operation
- High short circuit withstand capability

Applications

- Down Hole Oil Drilling, Geothermal Instrumentation
- Hybrid Electric Vehicles (HEV)
- Solar Inverters
- Switched-Mode Power Supply (SMPS)
- Power Factor Correction (PFC)
- Induction Heating
- Uninterruptible Power Supply (UPS)
- Motor Drives

Maximum Ratings at T_j = 250 °C, unless otherwise specified

Parameter	Symbol	Conditions	Values	Unit
Drain – Source Voltage	V _{DS}	V _{GS} = 0 V	650	V
Continuous Drain Current	I _D	T _C = 165 °C	4	A
Gate Peak Current	I _{GM}		5	A
Reverse Gate – Source Voltage	V _{GS}		200	V
Reverse Drain – Source Voltage	V _{DS}		40	V
Power Dissipation	P _{tot}	T _C = 25 °C	7	W
Operating and Storage Temperature	T _j , T _{stg}		-55 to 250	°C

Electrical Characteristics at T_j = 250 °C, unless otherwise specified

Parameter	Symbol	Conditions	Values		
			min.	typ.	max.
On Characteristics					
Drain – Source On Voltage	V _{DS(ON)}	I _D = 4 A, I _G = 100 mA, T _j = 25 °C I _D = 4 A, I _G = 250 mA, T _j = 175 °C I _D = 4 A, I _G = 250 mA, T _j = 250 °C	1.7 3.2 4.7		V
Drain – Source On Resistance	R _{DS(ON)}	I _D = 4 A, I _G = 100 mA, T _j = 25 °C I _D = 4 A, I _G = 250 mA, T _j = 175 °C I _D = 4 A, I _G = 250 mA, T _j = 250 °C	415 820 1310		mΩ
Gate Forward Voltage	V _{GS(FWD)}	I _G = 500 mA, T _j = 25 °C I _G = 500 mA, T _j = 250 °C	3.3 3.2		V
DC Current Gain	β	V _{DS} = 5 V, I _D = 5 A, T _j = 25 °C V _{DS} = 5 V, I _D = 5 A, T _j = 250 °C	120 85		

Off Characteristics

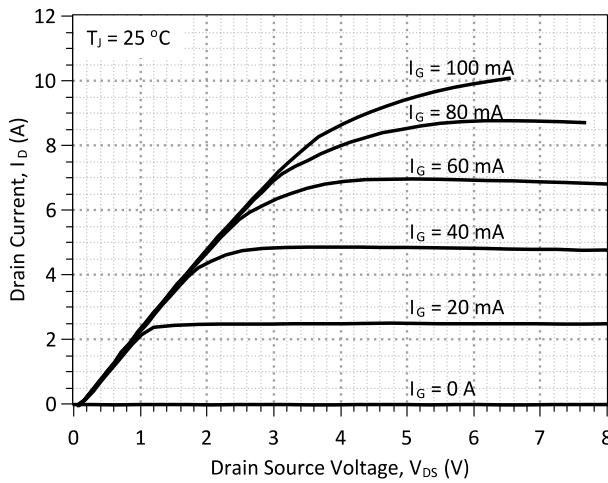
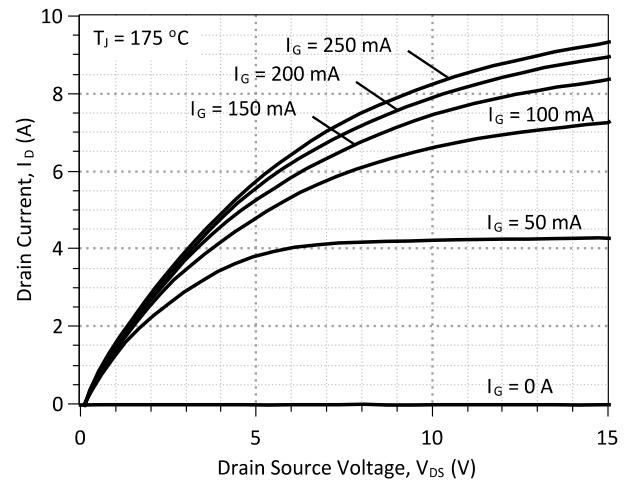
Drain Leakage Current	I _{DSS}	V _R = 650 V, V _{GS} = 0 V, T _j = 25 °C V _R = 650 V, V _{GS} = 0 V, T _j = 175 °C V _R = 650 V, V _{GS} = 0 V, T _j = 250 °C	7 25 105	nA
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Electrical Characteristics at $T_J = 250^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Conditions	Values		
			min.	typ.	max.
Dynamic Characteristics					
Input Capacitance	C_{iss}	$V_{DS} = 35 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}, T_J = 25^\circ\text{C}$	324		pF
Output Capacitance	C_{oss}		45		pF
Reverse Transfer Capacitance	C_{rss}		45		pF
Switching Characteristics					
Turn On Delay Time	$t_{d(on)}$	$V_{DD} = 400 \text{ V}, I_D = 5 \text{ A}, R_{G(on)} = R_{G(off)} = 44 \Omega, V_{GS} = -8/15 \text{ V}, T_J = 175^\circ\text{C}$ Refer to Figure 10 for gate drive current waveforms	5		ns
Rise Time	t_r		15		ns
Turn Off Delay Time	$t_{d(off)}$		74		ns
Fall Time	t_f		14		ns
Turn-On Energy Per Pulse	E_{on}		24		μJ
Turn-Off Energy Per Pulse	E_{off}		7		μJ
Total Switching Energy	E_{ts}		31		μJ
Turn On Delay Time	$t_{d(on)}$	$V_{DD} = 400 \text{ V}, I_D = 5 \text{ A}, R_{G(on)} = R_{G(off)} = 44 \Omega, V_{GS} = -8/15 \text{ V}, T_J = 250^\circ\text{C}$ Refer to Figure 10 for gate drive current waveforms	9		ns
Rise Time	t_r		24		ns
Turn Off Delay Time	$t_{d(off)}$		114		ns
Fall Time	t_f		17		ns
Turn-On Energy Per Pulse	E_{on}		54		μJ
Turn-Off Energy Per Pulse	E_{off}		10		μJ
Total Switching Energy	E_{ts}		64		μJ

Thermal Characteristics

Thermal resistance, junction - case	R_{thJC}	4.2	$^\circ\text{C/W}$
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Figure 1: Typical Output Characteristics at 25°C

Figure 2: Typical Output Characteristics at 175°C

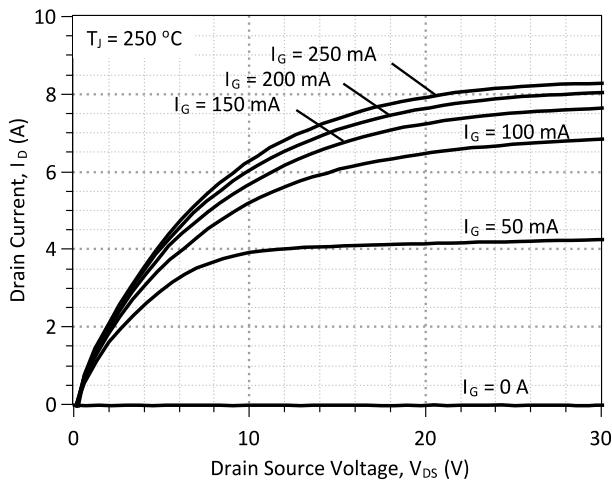


Figure 3: Typical Output Characteristics at 250°C

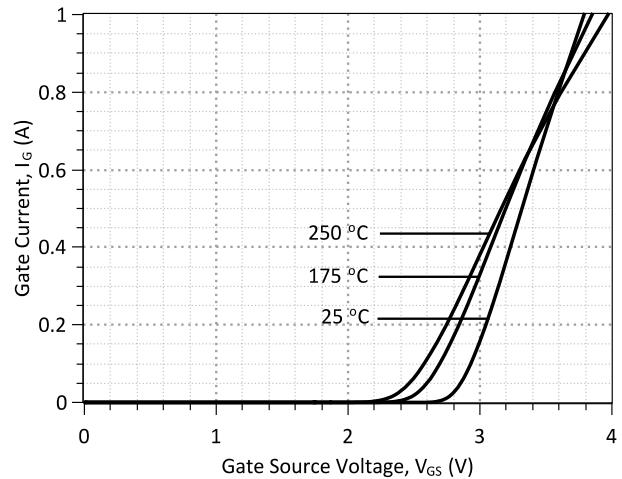


Figure 4: Typical Gate Source I-V Characteristics vs. Temperature

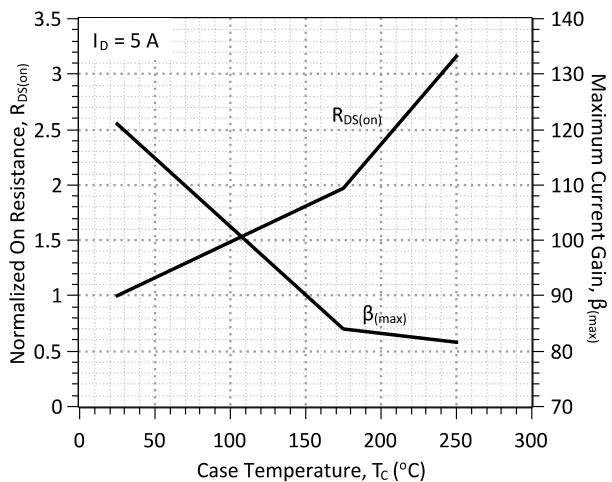


Figure 5: Normalized On-Resistance and Current Gain vs. Temperature

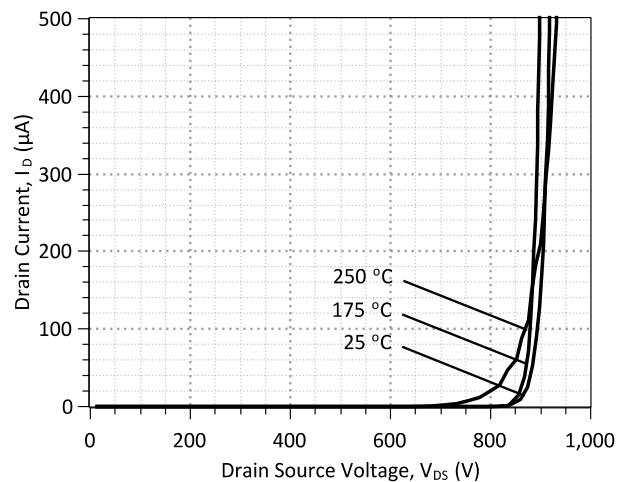


Figure 6: Typical Blocking Characteristics

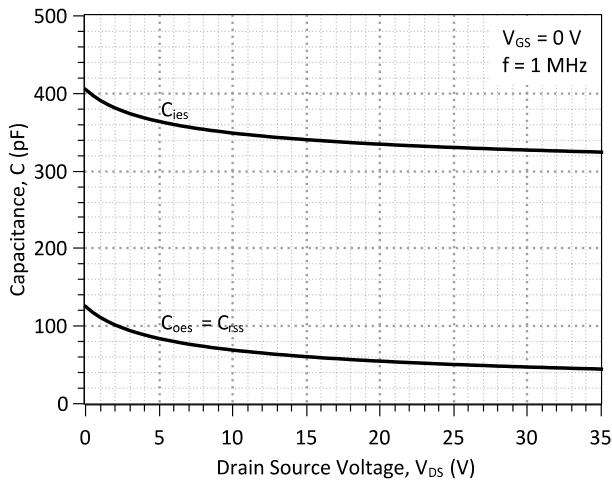


Figure 7: Typical Capacitance vs Drain-Source Voltage

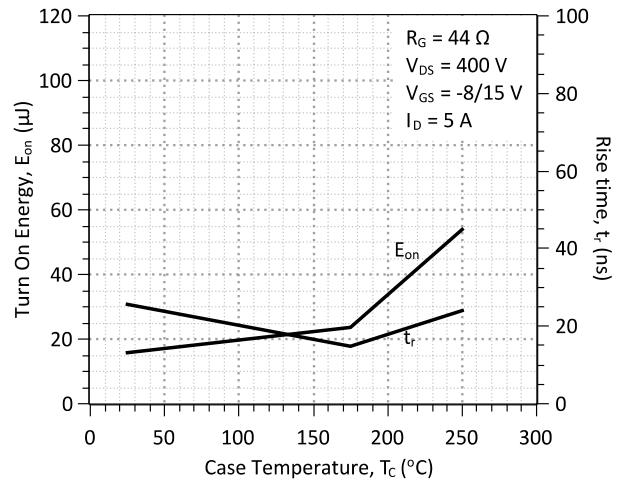
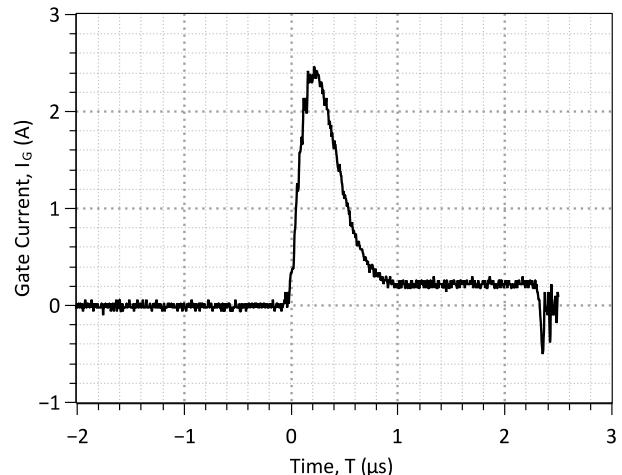
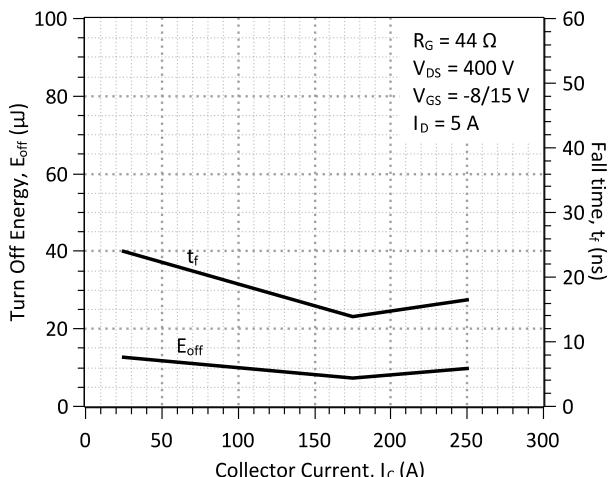
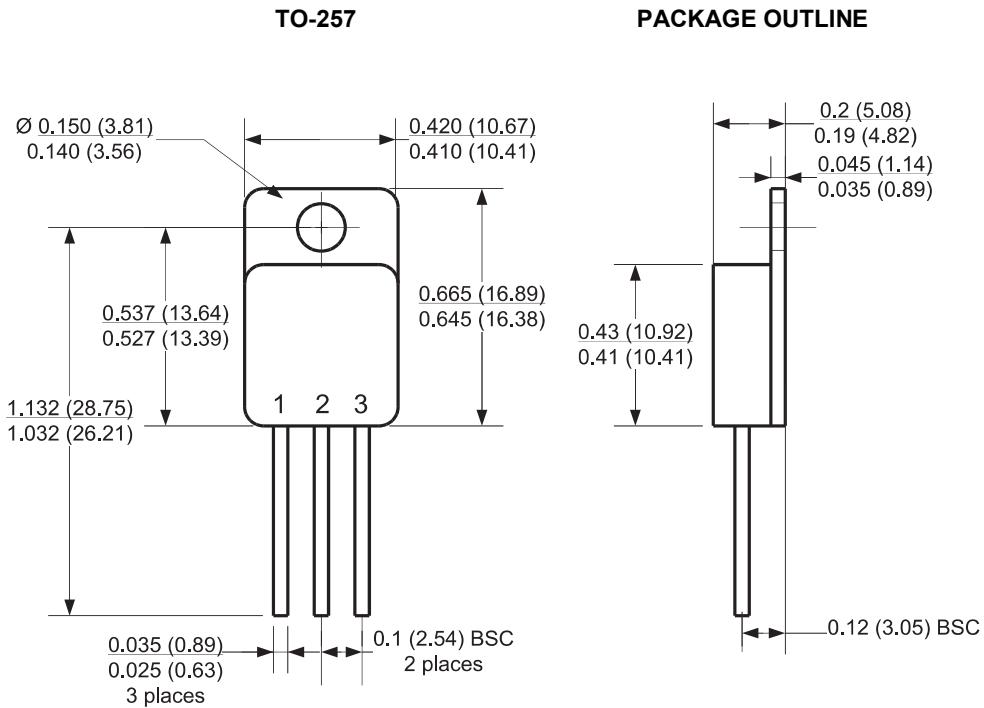


Figure 8: Typical Turn On Energy Losses and Switching Times vs. Temperature



Package Dimensions:



NOTE

1. CONTROLLED DIMENSION IS INCH. DIMENSION IN BRACKET IS MILLIMETER.
2. DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH, MATERIAL PROTRUSIONS

Revision History			
Date	Revision	Comments	Supersedes
2012/08/24	0	Initial release	

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