Fair-Rite Products Corp.
Your Signal Solution®

Chip Arrays (2512063017Y0A4)

Part Number: 2512063017Y0A4

MULTI- LAYER TAPE & REEL

Part Number System: Example 2512066007Y0A4

25	1206	600	7	Y	0	A4
Chip Suppression Component	Package Size	Impedance Code 600 = 60 Ω	Packaging Code 6= Bulk Packed 7= Taped and Reeled 7" Reel 8= Taped and Reeled 13" Reel	Material Code Y = Std Signal Speed	Current Code 0 < 1A	Array 4 Lines

Fair- Rite offers an effective cost and real estate reduction by our line of chip arrays. Four chip beads, packaged in a 1206 (3216) size, for suppression of conducted EMI where size is at a premium.

Chip arrays have plated contacts, 100% matte tin over a nickel undercoating.

The arrays can accommodate both reflow and wave soldering technologies.

Recommended storage and operating temperature range is -55 °C to 125 °C.

□□Chip Bead Kit□ (part number 0199000018) contains the 600 ohm 4 line chip array.

Packaging Options:
□- Chip arrays are supplied taped and reeled.

Suggested land patterns are in accordance to the IPC-7351.

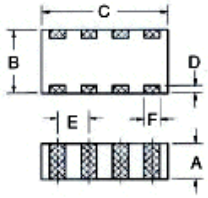
Weight: 0.03 (g)

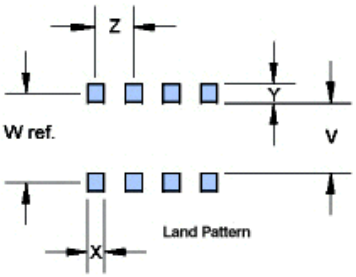
Package Size: 1206 (3216)

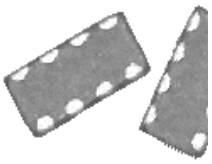
Dim	mm	mm tol	nominal inch	inch misc.
A	0.8	±0.20	0.032	_
B	1.6	±0.20	0.063	_
C	3.2	±0.20	0.126	_
D	0.3	±0.20	0.011	_
E	0.8	±0.10	0.032	_
F	0.04	±0.15	0.018	_

Land Patterns				
V	W	X	Y	Z
0.70 (0.028")	1.30 (0.051")	0.50 (0.020")	0.60 (0.024")	0.80 (0.032")

Reel Information				
Tape Width mm	Pitch mm	Parts 7" Reel	Parts 13" Reel	Parts 14" Reel
8	4	3000	--	--







Pkg. Size	A	B	C	D	E	F	Wt. (g)	Land Patterns					Reel Info	
								V	W (ref)	X	Y	Z	Tape Width mm	Pit mr
1206 (3216)	0.8±0.2 0.032	1.6±0.2 0.063	3.2±0.2 0.126	0.3±0.2 0.011	0.8±0.1 0.032	0.45±0.2 0.018	0.03	0.7 0.028	1.3 0.051	0.5 0.020	0.6 0.024	0.8 0.032	8	4

Chart Legend
+ Test frequency

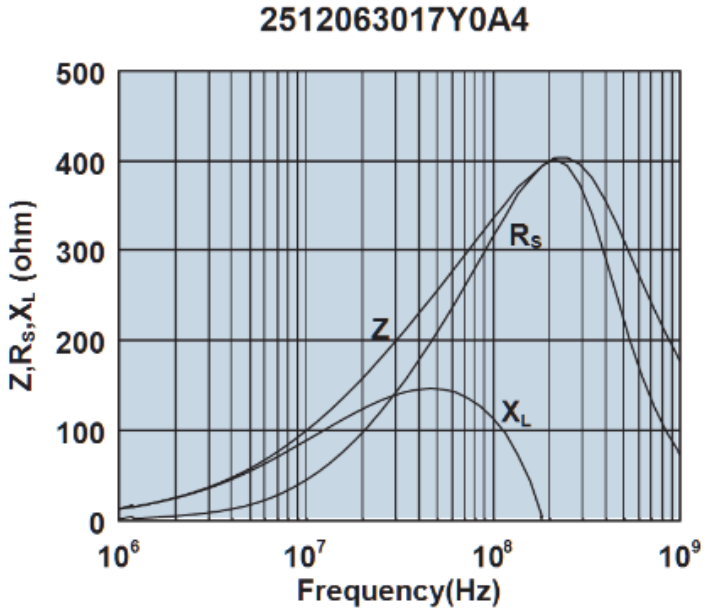
Typical Impedance (Ω)	
50 MHz	225
100 MHz+	300 ±25%
500 MHz	280
1000 MHz	160

Electrical Properties	
Max DCR (Ω)	0.3
Max Current (mA)	150

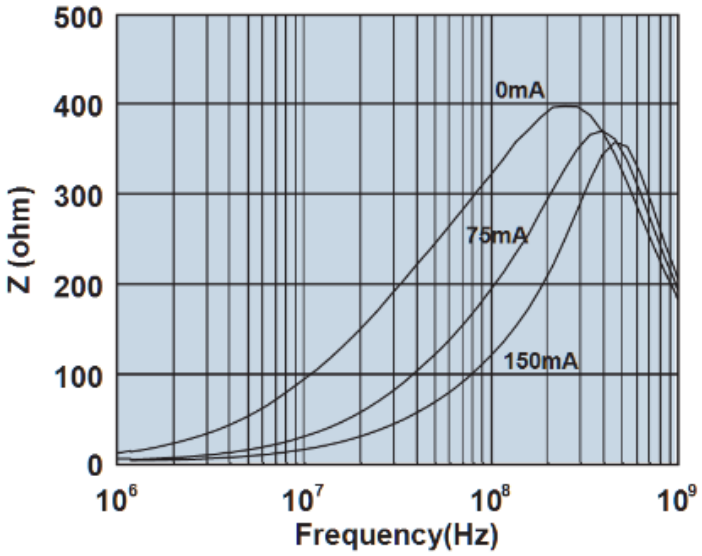
Chip arrays are controlled for impedance. The impedance values listed are typical values. The nominal impedance with a +/- 25% tolerance is specified for the + marked 100 MHz frequency. Chip arrays are measured for impedance on the HP 4291A and fixture HP 16192A.

The maximum voltage between adjacent beads is 5V.

Chip arrays are 100% tested for impedance and dc resistance.



Impedance, reactance, and resistance vs. frequency.



Impedance vs. frequency with dc bias.