TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74ACT574P,TC74ACT574F,TC74ACT574FT

Octal D-Type Flip-Flop with 3-State Output

The TC74ACT574 is an advanced high speed CMOS OCTAL FLIP-FLOP fabricated with silicon gate and double-layer metal wiring C^2MOS technology.

It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

This devices may be used as a level converter for interfacing TTL or NMOS to High Speed CMOS. The inputs are compatible with TTL, NMOS and CMOS output voltage levels.

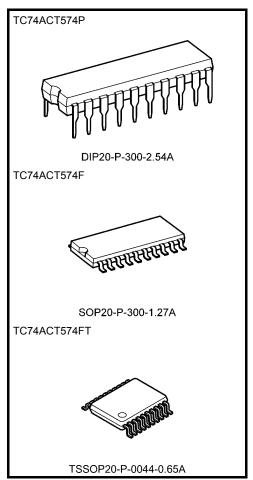
These 8-bit D-type flip-flops are controlled by a clock input (CK) and a output enable input (\overline{OE}).

When the \overline{OE} input is high, the eight outputs are in a high impedance state.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

Features

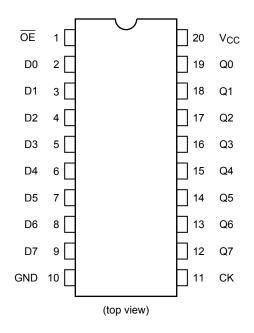
- High speed: $f_{max} = 180 \text{ MHz}$ (typ.) at $V_{CC} = 5 \text{ V}$
- Low power dissipation: $I_{CC} = 8 \mu A \text{ (max)}$ at $T_{a} = 25 \text{°C}$
- Oompatible with TTL outputs: $V_{IL} = 0.8 \ V \ (\text{max}), \ V_{IH} = 2.0 \ V \ (\text{min})$
- Symmetrical output impedance: $|I_{OH}| = I_{OL} = 24 \text{ mA (min) Capability of driving 50 } \Omega$ transmission lines.
- Balanced propagation delays: $t_{pLH} \simeq t_{pHL}$
- Pin and function compatible with 74F574



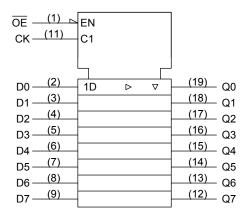
Weight

DIP20-P-300-2.54A : 1.30 g (typ.) SOP20-P-300-1.27A : 0.22 g (typ.) TSSOP20-P-0044-0.65A : 0.08 g (typ.)

Pin Assignment



IEC Logic Symbol



Truth Table

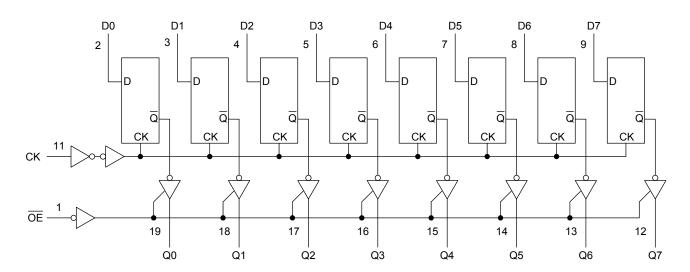
	Inputs	Output				
ŌE	CK	D	Q			
Н	Х	Х	Z			
L	\rightarrow	Х	Qn			
L		L	L			
L		Н	Н			

X: Don't care

Z: High impedance

Q_n: No change

System Diagram





Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	−0.5 to 7.0	V
DC input voltage	V _{IN}	-0.5 to V _{CC} + 0.5	V
DC output voltage	V _{OUT}	-0.5 to V _{CC} + 0.5	V
Input diode current	I _{IK}	±20	mA
Output diode current	lok	±50	mA
DC output current	lout	±50	mA
DC V _{CC} /ground current	Icc	±200	mA
Power dissipation	PD	500 (DIP) (Note 2)/180 (SOP/TSSOP)	mW
Storage temperature	T _{stg}	−65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of Ta = -40 to 65°C. From Ta = 65 to 85°C a derating factor of -10 mW/°C should be applied up to 300 mW.

Operating Ranges (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	4.5 to 5.5	V
Input voltage	V _{IN}	0 to V _{CC}	V
Output voltage	V _{OUT}	0 to V _{CC}	V
Operating temperature	T _{opr}	−40 to 85	°C
Input rise and fall time	dt/dV	0 to 10	ns/V

Note: The operating ranges must be maintained to ensure the normal operation of the device.
Unused inputs must be tied to either VCC or GND.

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Electrical Characteristics

DC Characteristics

Characteristics Symbol		Test Condition				Ta = 25°C		Ta = −40 to 85°C		Unit	
					V _{CC} (V)	Min	Тур.	Max	Min	Max	OTHE
High-level input voltage	V _{IH}	_		4.5 to 5.5	2.0	1	-	2.0	-	V	
Low-level input voltage	V _{IL}	_		4.5 to 5.5	_	_	0.8	_	0.8	V	
	V _{OH}	V _{IN} = V _{IH} or V _{IL}	I _{OH} = -50 μA		4.5	4.4	4.5	_	4.4	_	V
High-level output voltage			I _{OH} = −24 mA		4.5	3.94	_	_	3.80	_	
			I _{OH} = -75 mA	(Note)	5.5	_	_	_	3.85	_	
	V _{OL}	V _{IN} = V _{IH} or V _{IL}	I _{OL} = 50 μA I _{OL} = 24 mA		4.5	_	0.0	0.1	_	0.1	
Low-level output voltage					4.5	_	_	0.36	_	0.44	V
remage			I _{OL} = 75 mA	(Note)	5.5	_	_	_	_	1.65	
3-state output off-state current	I _{OZ}	V _{IN} = V _{IH} or V _{IL} V _{OUT} = V _{CC} or GND		5.5	ı		±0.5	l	±5.0	μΑ	
Input leakage current	I _{IN}	V _{IN} = V _{CC} or GND		5.5	1	1	±0.1	-	±1.0	μΑ	
Quiescent supply current	Icc	V _{IN} = V _{CC} or GND			5.5		_	8.0	_	80.0	μA
	Ic	Per input: V _{IN} = 3.4 V Other input: V _{CC} or GND		5.5	_	_	1.35	_	1.5	mA	

Note: This spec indicates the capability of driving 50 Ω transmission lines.

One output should be tested at a time for a 10 ms maximum duration.

Timing Requirements (input: $t_r = t_f = 3 \text{ ns}$)

Characteristics	Symbol	Test Condition	Test Condition		Ta = -40 to 85°C	Unit
			V _{CC} (V)	Limit	Limit	
Minimum pulse width	t _{w (H)}	_	5.0 ± 0.5	5.0	5.0	ns
(CK)	t _{w (L)}		3.0 1 0.0	5.0	5.0	10
Minimum set-up time	ts	_	5.0 ± 0.5	3.0	3.0	ns
Minimum hold time	t _h	_	5.0 ± 0.5	2.0	2.0	ns

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AC Characteristics (C_L = 50 pF, R_L = 500 Ω , input: t_r = t_f = 3 ns)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
	,		V _{CC} (V)	Min	Тур.	Max	Min	Max	
Propagation delay time (CK-Q)	t _{pLH}	_	5.0 ± 0.5	_	6.2	10.1	1.0	11.5	ns
Output enable time	t _{pZL} t _{pZH}	_	5.0 ± 0.5	_	6.3	10.5	1.0	12.0	ns
Output disable time	t _{pLZ}	_	5.0 ± 0.5	_	6.6	9.6	1.0	11.0	ns
Maximum clock frequency	f _{max}	_	5.0 ± 0.5	85	160	_	85	_	MHz
Input capacitance	C _{IN}	_		_	5	10	_	10	pF
Output capacitance	C _{OUT}	_		_	10	_	_	_	pF
Power dissipation capacitance	C _{PD}		(Note)	_	33	_	_	_	pF

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

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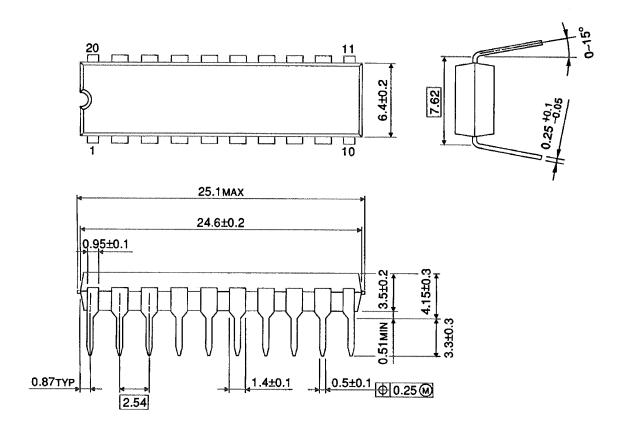
Average operating current can be obtained by the equation:

$$I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/8 (per F/F)$$

And the total C_{PD} when n pcs. of F/F operate can be gained by the following equation:

Package Dimensions

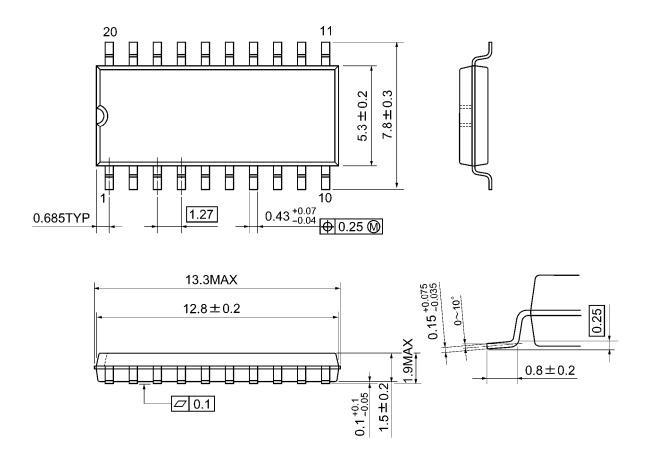
DIP20-P-300-2.54A Unit: mm



Weight: 1.30 g (typ.)

Package Dimensions

SOP20-P-300-1.27A Unit: mm

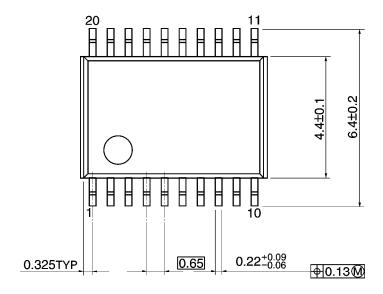


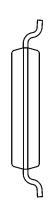
Weight: 0.22 g (typ.)

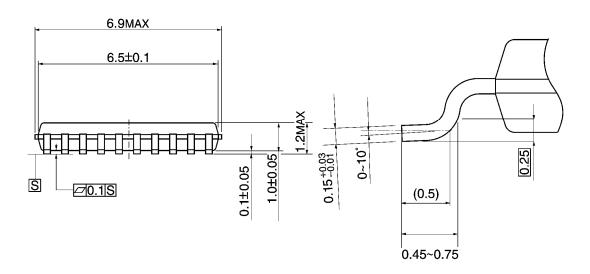
Package Dimensions

TSSOP20-P-0044-0.65A

Unit: mm







Weight: 0.08 g (typ.)

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