T-1 (3mm) SOLID STATE LAMP

Part Number: WP132XGC

Green

Features Description • Low power consumption. The Green source color devices are made with Gallium • Popular T-1 diameter package. Phosphide Green Light Emitting Diode. • General purpose leads. • Reliable and rugged. • Long life-solid state reliability. • Available on tape and reel. • RoHS compliant. **Package Dimensions** 5.2[0.205] 27[1.063]MIN. 1.1[0.043] 1.5[0.059]TYP. CATHODE ø3.8[0.15] 2.54[0.1] - C ø3.5[0.138] 0.5[0.02] ø3[0.118] 0.7MAX 1.0MAX. Notes: 1. All dimensions are in millimeters (inches). 2. Tolerance is ±0.25(0.01") unless otherwise noted. Lead spacing is measured where the leads emerge from the package. The specifications, characteristics and technical data described in the datasheet are subject to change without prior notice.

SPEC NO: DSAF1654 APPROVED: WYNEC REV NO: V.3 CHECKED: Allen Liu DATE: FEB/28/2011 DRAWN: J.Yu



Selection Guide								
Part No.	Dice	Lens Type	lv (mcd) [2] @ 10mA		Viewing Angle [1]			
			Min.	Тур.	201/2			
WP132XGC	Green (GaP)	Water Clear	20	40	50°			

Notes:

01/2 is the angle from optical centerline where the luminous intensity is 1/2 of the optical peak value.
Luminous intensity/ luminous Flux: +/-15%.

Electrical / Optical Characteristics at TA=25°C

Symbol	Parameter	Device	Тур.	Max.	Units	Test Conditions
λpeak	Peak Wavelength	Green	565		nm	l⊧=20mA
λD [1]	Dominant Wavelength	Green	568		nm	l⊧=20mA
Δλ1/2	Spectral Line Half-width	Green	30		nm	l⊧=20mA
С	Capacitance	Green	15		pF	VF=0V;f=1MHz
VF [2]	Forward Voltage	Green	2.2	2.5	V	l⊧=20mA
lr	Reverse Current	Green		10	uA	VR = 5V

Notes:

1.Wavelength: +/-1nm. 2. Forward Voltage: +/-0.1V.

Absolute Maximum Ratings at TA=25°C

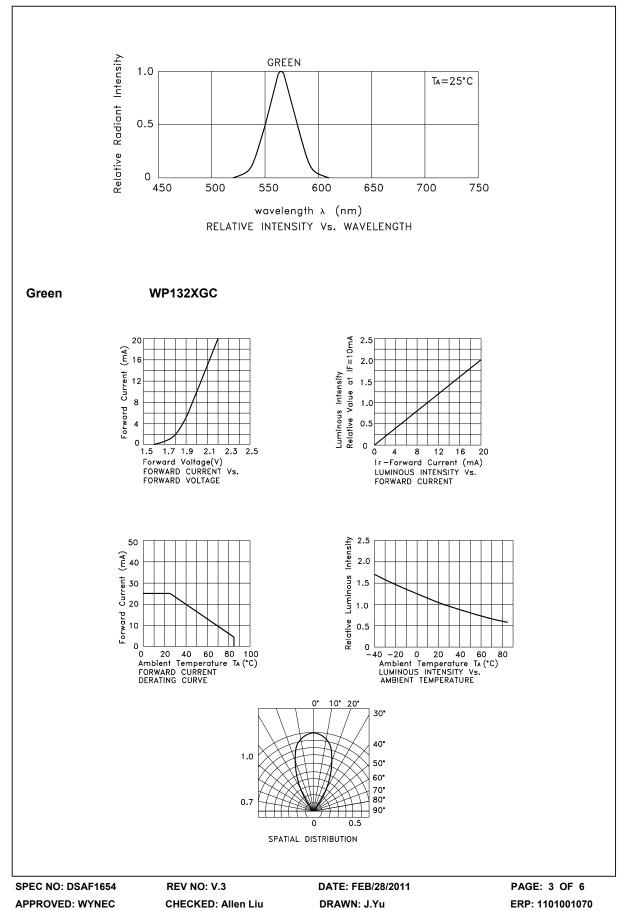
Parameter	Green		
Power dissipation	dissipation 62.5		
DC Forward Current	25	mA	
Peak Forward Current [1]	140	mA	
Reverse Voltage	5	V	
perating/Storage Temperature -40°C To +85°C			
Lead Solder Temperature [2]	Solder Temperature [2] 260°C For 3 Seconds		
Lead Solder Temperature [3]	260°C For 5 Seconds		
Notes: 1. 1/10 Duty Cycle, 0.1ms Pulse Width. 2. 2mm below package base			

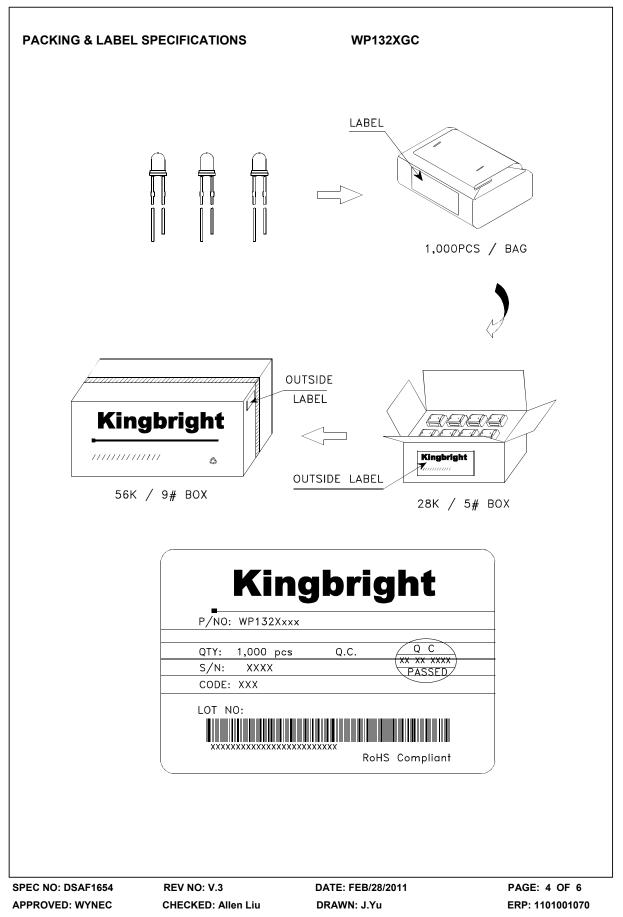
2. 2mm below package base.
3. 5mm below package base.

SPEC NO: DSAF1654 APPROVED: WYNEC

REV NO: V.3 CHECKED: Allen Liu DATE: FEB/28/2011 DRAWN: J.Yu

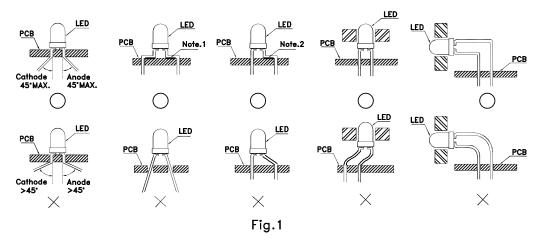
PAGE: 2 OF 6 ERP: 1101001070





PRECAUTIONS

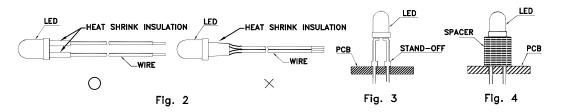
1. The lead pitch of the LED must match the pitch of the mounting holes on the PCB during component placement. Lead-forming may be required to insure the lead pitch matches the hole pitch. Refer to the figure below for proper lead forming procedures. (Fig. 1)



"〇

Correct mounting method "imes" Incorrect mounting method

- When soldering wire to the LED, use individual heat-shrink tubing to insulate the exposed leads to prevent accidental contact short-circuit. (Fig.2)
- 3.Use stand-offs (Fig.3) or spacers (Fig.4) to securely position the LED above the PCB.



- 4. Maintain a minimum of 2mm clearance between the base of the LED lens and the first lead bend. (Fig. 5 and 6)
- 5. During lead forming, use tools or jigs to hold the leads securely so that the bending force will not be transmitted to the LED lens and its internal structures. Do not perform lead forming once the component has been mounted onto the PCB. (Fig. 7)

SPEC NO: DSAF1654 APPROVED: WYNEC REV NO: V.3 CHECKED: Allen Liu DATE: FEB/28/2011 DRAWN: J.Yu

