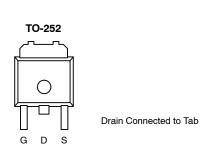


Vishay Siliconix

N-Channel 22-V (D-S) 175°C MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	$r_{DS(on)}\left(\Omega\right)$	I _D (A) ^d		
24 ^C	0.006 @ V _{GS} = 10 V	80		
	0.0095 @ V _{GS} = 4.5 V	64		



SUD50N024-06P Ordering Information:

Top View

SUD50N024-06P—E3 (Lead (Pb)-Free)

FEATURES

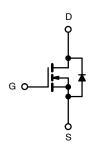


Available

- TrenchFET® Power MOSFET
- 175°C Junction Temperature
- PWM Optimized for High Efficiency
- 100% R_g Tested
 Lead (Pb)-Free Version is RoHS Compliant

APPLICATIONS

- Synchronous Buck DC/DC Conversion
 - Desktop
 - Server



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T _A = 25°C UNLESS OTHERWISE NOTED)						
Parameter		Symbol	Limit	Unit		
Drain-Source Pulse Voltage	V _{DS(pulse)}	24 ^C				
Drain-Source Voltage		V _{DS}	22	V		
Gate-Source Voltage		V _{GS}	±20			
0 0 0	T _C = 25°C		80 ^d			
Continuous Drain Current ^a	T _C = 100°C	I _D	56 ^d			
Pulsed Drain Current		I _{DM}	100	A		
Continuous Source Current (Diode Conduction) ^a		I _S	26			
Avalanche Current, Single Pulse	L = 0.1 mH	I _{AS}	45			
Avalanche Energy, Single Pulse		E _{AS}	101	mJ		
Mariana Dania Dininata	T _A = 25°C		6.8 ^a	14/		
Maximum Power Dissipation	T _C = 25°C	- P _D	65	W		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to 175	°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
	t ≤ 10 sec	R _{thJA}	18	22	°C/W	
Maximum Junction-to-Ambient ^a	Steady State		40	50		
Maximum Junction-to-Case		R _{thJC}	1.9	2.3		

- Surface Mounted on FR4 Board, $t \le 10$ sec.
- Limited by package
- Pulse condition: T_A = 105°C, 50 ns, 300 kHz operation Calculation based on maximum allowable Junction Temperature. Package limitation current is 50 A.

SUD50N024-06P

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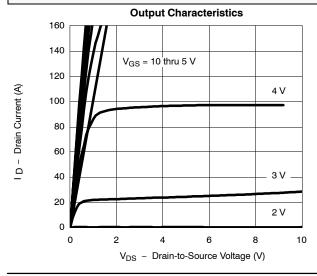
Parameter	Symbol	Test Condition	Min	Typa	Max	Unit	
	Syllibol	rest Condition	IVIIII	тур"	IVIAX	Offic	
Static							
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V_{GS} = 0 V, I_D = 250 μA	22			V	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	0.8		3.0		
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			±100	nA	
Zero Gate Voltage Drain Current	1 .	V _{DS} = 20 V, V _{GS} = 0 V			1		
	I _{DSS}	$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 125^{\circ}\text{C}$			50	μA	
On-State Drain Current ^b	I _{D(on)}	V _{DS} = 5 V, V _{GS} = 10 V	50			Α	
		$V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}$		0.0046	0.006		
Drain-Source On-State Resistance ^b	r _{DS(on)}	$V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}, T_J = 125^{\circ}\text{C}$			0.0084	0.0084 Ω	
		V _{GS} = 4.5 V, I _D = 20 A		0.0073	0.0095		
Forward Transconductanceb	9fs	V _{DS} = 15 V, I _D = 20 A	15			S	
Dynamic ^a							
Input Capacitance	C _{iss}			2550		pF	
Output Capacitance	C _{oss}	$V_{GS} = 0 \text{ V}, V_{DS} = 10 \text{ V}, f = 1 \text{ MHz}$		900			
Reverse Transfer Capacitance	C _{rss}			415			
Gate Resistance	R _g		0.7	1.5	2.1	Ω	
Total Gate Charge ^c	Qg			19	30	nC	
Gate-Source Charge ^c	Q _{gs}	$V_{DS} = 10 \text{ V}, \ V_{GS} = 4.5 \text{ V}, \ I_D = 50 \text{ A}$		7.5			
Gate-Drain Charge ^c	Q _{gd}			6.0			
Turn-On Delay Time ^c	t _{d(on)}			11	20		
Rise Time ^c	t _r	$V_{DD} = 10 \text{ V, R}_{L} = 0.2 \Omega$		10	15	ns	
Turn-Off Delay Time ^c	t _{d(off)}	V_{DD} = 10 V, R_L = 0.2 Ω $I_D \cong 50$ A, V_{GEN} = 10 V, R_g = 2.5 Ω		24	35		
Fall Time ^c	t _f			9	15		
Source-Drain Diode Ratings and	d Characteristic	c (T _C = 25°C)	•	•	•	•	
Pulsed Current	I _{SM}				100	Α	
Diode Forward Voltageb	V _{SD}	I _F = 50 A, V _{GS} = 0 V		1.2	1.5	V	
Source-Drain Reverse Recovery Time	t _{rr}	I _F = 50 A, di/dt = 100 A/μs	1	35	70	ns	

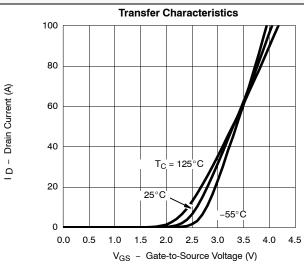
Notes

- Guaranteed by design, not subject to production testing. Pulse test; pulse width $\leq 300~\mu s$, duty cycle $\leq 2\%$. Independent of operating temperature. a.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)



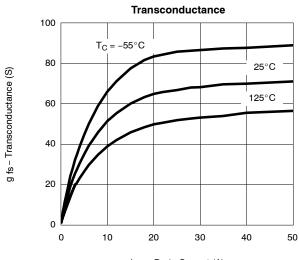




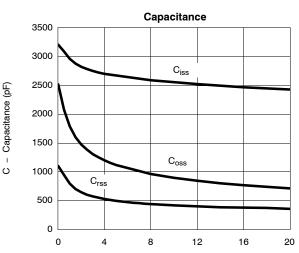


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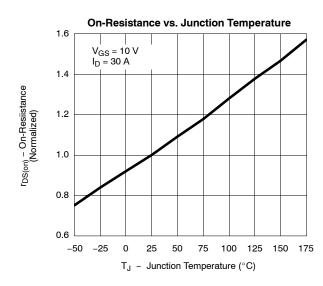
TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)





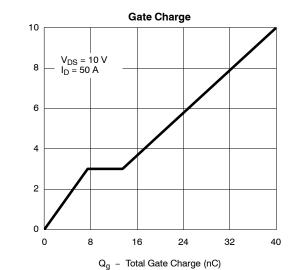


V_{DS} - Drain-to-Source Voltage (V)



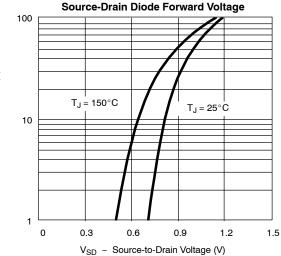
On-Resistance vs. Drain Current 0.010 rDS(on)- On-Resistance (Ω) 0.008 $V_{GS} = 4.5 \text{ V}$ 0.006 V_{GS} = 6.3 V0.004 $V_{GS} = 10 \text{ V}$ 0.002 0.000 0 20 40 60 80 100

I_D - Drain Current (A)





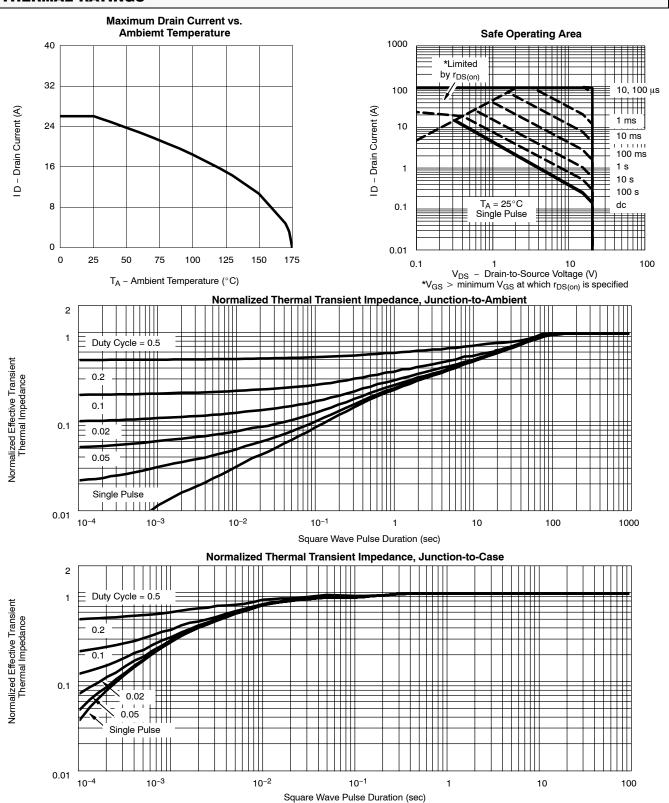
VGS - Gate-to-Source Voltage (V)



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THERMAL RATINGS



Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see http://www.vishay.com/ppg?72289.



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