

# BC856BMB

# 60 V, 100 mA PNP general-purpose transistor

**Product data sheet** 

### 1. General description

PNP general-purpose transistor in a leadless ultra small DFN1006B-3 (SOT883B) Surface-Mounted Device (SMD) plastic package.

NPN complement: BC846BMB.

#### 2. Features and benefits

- Leadless ultra small SMD plastic package
- Low package height of 0.37 mm
- Power dissipation comparable to SOT23
- AEC-Q101 qualified

### 3. Applications

- General-purpose switching and amplification
- Mobile applications

#### 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-	-60	V
I <sub>C</sub>	collector current		-	-	-100	mA
h <sub>FE</sub>	DC current gain	$V_{CE}$ = -5 V; $I_{C}$ = -2 mA; $T_{amb}$ = 25 °C	220	-	475	

### 5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base	1 🔲	3
2	E	emitter	2 3	1—
3	С	collector	Transparent top view	2
			DFN1006B-3 (SOT883B)	sym013



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# 6. Ordering information

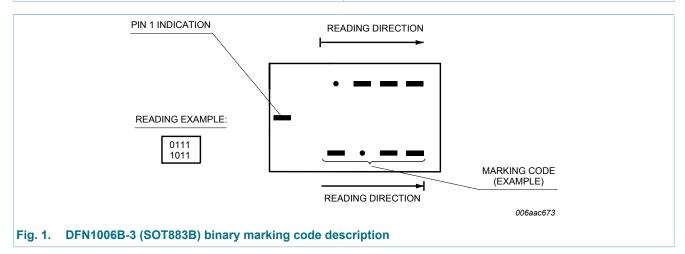
Table 3. Ordering information

Type number	Package				
	Name	Description	Version		
BC856BMB	DFN1006B-3	DFN1006B-3: leadless ultra small plastic package; 3 solder lands; body 1.0 x 0.6 x 0.37 mm	SOT883B		

# 7. Marking

Table 4. Marking codes

Type number	Marking code
BC856BMB	0101 1010



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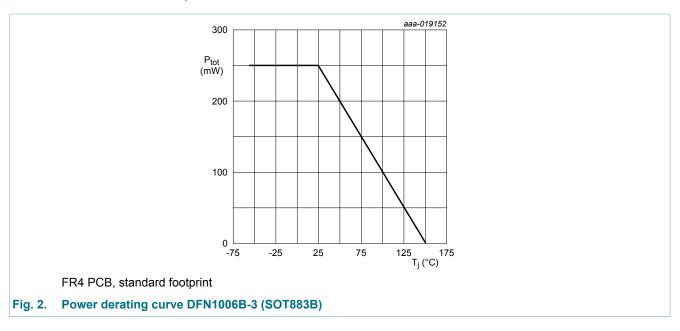
# 8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
$V_{CBO}$	collector-base voltage	open emitter		-	-80	V
$V_{CEO}$	collector-emitter voltage	open base		-	-60	V
$V_{EBO}$	emitter-base voltage	open collector		-	-6	V
I <sub>C</sub>	collector current			-	-100	mA
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	-200	mA
I <sub>BM</sub>	peak base current			-	-200	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	250	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.



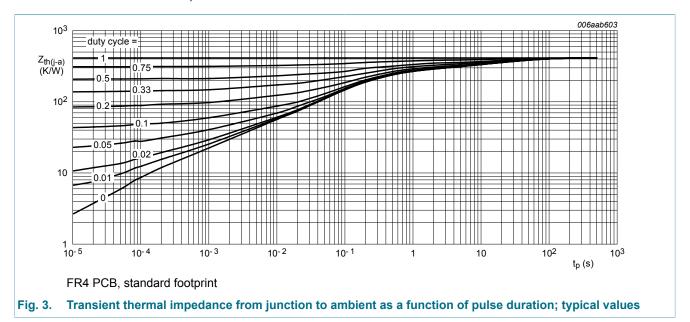
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### 9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	[1]	-	-	500	K/W

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.



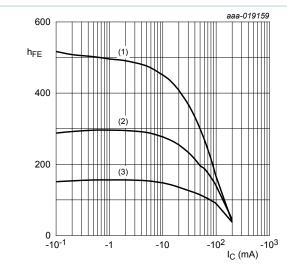
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### 10. Characteristics

#### Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I <sub>CBO</sub>	collector-base cut-off	$V_{CB}$ = -30 V; $I_E$ = 0 A; $T_{amb}$ = 25 °C	-	-	-15	nA
current	current	$V_{CB} = -30 \text{ V}; I_E = 0 \text{ A}; T_j = 150 ^{\circ}\text{C}$	-	-	-5	μA
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = -5 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C	-	-	-100	nA
h <sub>FE</sub>	DC current gain	$V_{CE}$ = -5 V; $I_{C}$ = -2 mA; $T_{amb}$ = 25 °C	220	-	475	
V <sub>CEsat</sub>	collector-emitter	$I_C$ = -10 mA; $I_B$ = -0.5 mA; $T_{amb}$ = 25 °C	-	-	-200	mV
	saturation voltage	$I_{C}$ = -100 mA; $I_{B}$ = -5 mA; pulsed; $t_{p}$ ≤ 300 µs; $\delta$ ≤ 0.02; $T_{amb}$ = 25 °C	-	-	-400	mV
V <sub>BEsat</sub>	base-emitter saturation	$I_C$ = -10 mA; $I_B$ = -0.5 mA; $T_{amb}$ = 25 °C	-	-700	-	mV
	voltage	$I_C$ = -100 mA; $I_B$ = -5 mA; $T_{amb}$ = 25 °C	-	-850	-	mV
V <sub>BE</sub>	base-emitter voltage	$V_{CE}$ = -5 V; $I_{C}$ = -2 mA; $T_{amb}$ = 25 °C	-600	-	-750	mV
		$V_{CE}$ = -5 V; $I_{C}$ = -10 mA; $T_{amb}$ = 25 °C	-	-	-820	mV
C <sub>C</sub>	collector capacitance	$V_{CB} = -10 \text{ V; } I_E = 0 \text{ A; } i_e = 0 \text{ A;}$ $f = 1 \text{ MHz; } T_{amb} = 25 \text{ °C}$	-	-	2.5	pF
C <sub>E</sub>	emitter capacitance	$V_{EB}$ = -0.5 V; $I_{C}$ = 0 A; $i_{c}$ = 0 A; f = 1 MHz; $T_{amb}$ = 25 °C	-	4.5	-	pF
f <sub>T</sub>	transition frequency	$V_{CE}$ = -5 V; $I_{C}$ = -10 mA; f = 100 MHz; $T_{amb}$ = 25 °C	100	-	-	MHz
NF	noise figure	$V_{CE}$ = -5 V; $I_{C}$ = -200 $\mu$ A; $R_{S}$ = 2 k $\Omega$ ; f = 1 kHz; B = 200 Hz; $T_{amb}$ = 25 °C	-	-	10	dB

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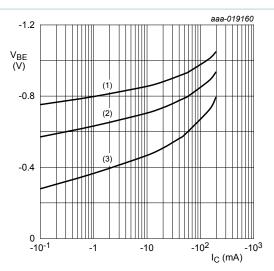
$$V_{CE}$$
 = -5  $V$ 

(1) 
$$T_{amb} = 150 \, ^{\circ}C$$

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

(3) 
$$T_{amb} = -55$$
 °C

Fig. 4. DC current gain as a function of collector current; typical values



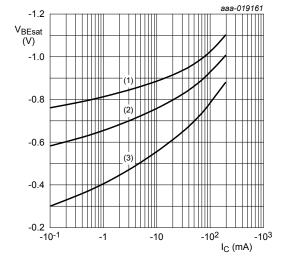
$$V_{CE}$$
 = -5  $V$ 

(1) 
$$T_{amb} = -55 \, ^{\circ}C$$

(2) 
$$T_{amb} = 25 \, ^{\circ}C$$

(3) 
$$T_{amb}$$
 = 150 °C

Fig. 5. Base-emitter voltage as a function of collector current; typical values



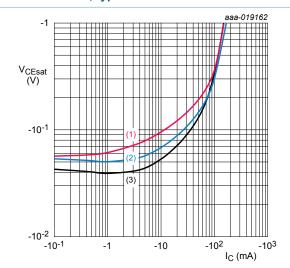
$$I_{\rm C}/I_{\rm B} = 20$$

(1) 
$$T_{amb} = -55 \,^{\circ}C$$

(2) 
$$T_{amb}$$
 = 25 °C

(3) 
$$T_{amb} = 150^{\circ}C$$

Fig. 6. Base-emitter saturation voltage as a function of collector current; typical values



$$I_{\rm C}/I_{\rm B} = 20$$

(1) 
$$T_{amb} = 150 \, ^{\circ}C$$

(2) 
$$T_{amb}$$
 = 25 °C

$$(3) T_{amb} = -55 °C$$

Fig. 7. Collector-emitter saturation voltage as a function of collector current; typical values

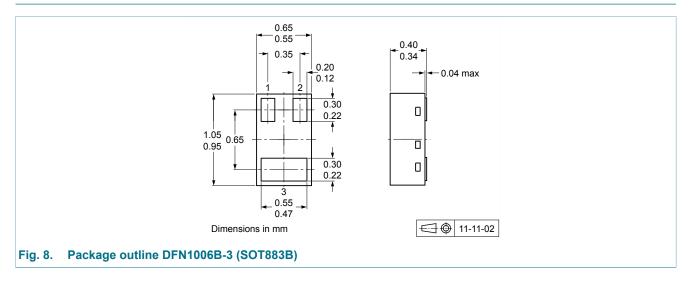
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### 11. Test information

### 11.1 Quality information

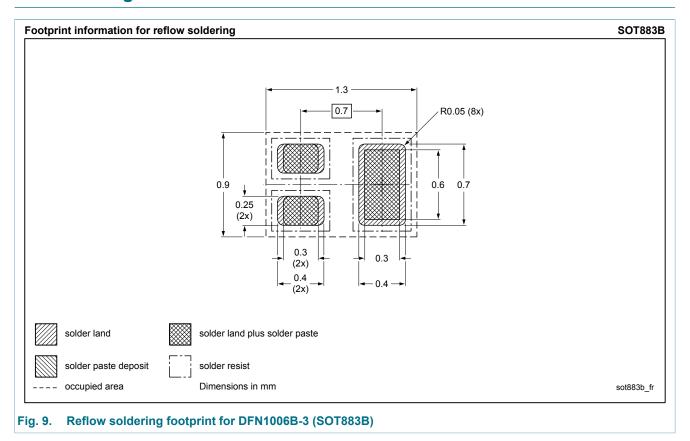
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

# 12. Package outline



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### 13. Soldering



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# 14. Revision history

#### Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
BC856BMB v.1	20150819	Product data sheet	-	-

### 60 V, 100 mA PNP general-purpose transistor

### 15. Legal information

#### 15.1 Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
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